BPC2425M7X60

Power LDMOS module

Rev. 1 — 29 March 2018

AMPLEON

Product data sheet

1. Product profile

1.1 General description

60 W LDMOS power module with excellent gain flatness for Industrial, Scientific and Medical (ISM) applications at frequencies from 2400 MHz to 2500 MHz. The module is designed as a dual stage high gain medium power amplifier for CW and pulsed applications.

Table 1. Test information

Typical RF performance at V_{DS} = 32 V; T_{mb} = 25 °C; $I_{Dq1(A)} = I_{Dq1(B)}$ = 25 mA; $I_{Dq2(A)} = I_{Dq2(B)}$ = 50 mA.

Test signal	f	V_{DS}	P_L	G_p	η_D
	(MHz)	(V)	(W)	(dB)	(%)
CW	2450	32	60	26	41
CW pulsed [1]	2450	32	60	26.5	42

[1] Pulse width is 300 μs; duty cycle is 50 %.

1.2 Features and benefits

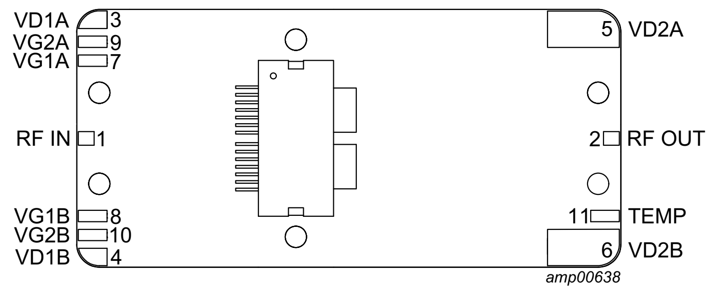
- Flat gain
- Small size: 72 × 34 mm
- Input/output 50 Ω matched
- Balanced configuration
- Designed for broadband operation (2400 MHz to 2500 MHZ)
- Built-in temperature sensor
- Built-in temperature compensation in biasing networks
- 100 % RF testing in production
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

- RF power amplifiers for CW applications in the 2400 MHz to 2500 MHz frequency range such as industrial heating and drying, scientific, medical, plasma lighting and solid state cooking

2. Pinning information

2.1 Pinning



Top view.

Fig 1. Pin configuration

2.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
RF IN	1	RF input
RF OUT	2	RF output
VD1A	3	drain-source voltage driver, section A
VD1B	4	drain-source voltage driver, section B
VD2A	5	drain-source voltage final, section A
VD2B	6	drain-source voltage final, section B
VG1A	7	gate-source voltage driver, section A
VG1B	8	gate-source voltage driver, section B
VG2A	9	gate-source voltage final, section A
VG2B	10	gate-source voltage final, section B
TEMP	11	temperature sensor

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BPC2425M7X60	-	pallet LDMOS; 6 mounting holes; 11 terminations	-

4. Block diagram

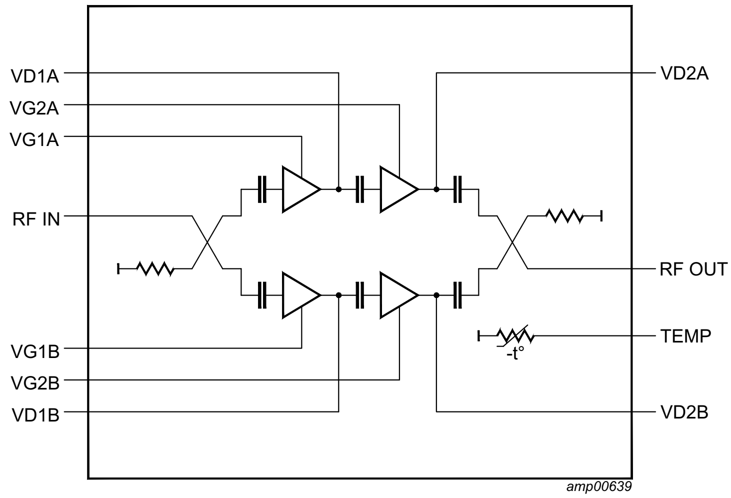


Fig 2. Block diagram

5. Limiting values

Table 4. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	non operating	0	65	V
V_{GS}	gate-source voltage	non operating	–6	+13	V
T_{stg}	storage temperature		–65	+85	°C
T_{mb}	mounting base temperature		0	85	°C

6. Characteristics

Table 5. DC characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	V_{GS} = 0 V; I_D = 2.7 mA	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	driver (VG1A, VG1B): V_{DS} = 32 V; I_D = 25 mA	-	1.95	-	V
		final (VG2A, VG2B); V_{DS} = 32 V; I_D = 50 mA	-	1.85	-	V
I_{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 32 V	-	-	4.20	μA
R_{GS}	gate-source resistance		300	1500	5000	Ω
C_{iss}	input capacitance	VG1A, VG2B pins	-	0.01	-	μF
		VD1A, VD2B pins	-	0.47	-	μF

Table 6. RF Characteristics

Test signal: CW; RF performance at T_{mb} = 25 °C; V_{DS} = 32 V; $I_{Dq1(A)}$ = $I_{Dq1(B)}$ = 25 mA; $I_{Dq2(A)}$ = $I_{Dq2(B)}$ = 50 mA; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G_p	power gain	P_L = 60 W; f = 2400 MHz to f = 2500 MHz	25	26	-	dB
$P_{L(1dB)}$	output power at 1 dB gain compression	f = 2400 MHz to f = 2500 MHz	-	80	-	W
$P_{L(3dB)}$	output power at 3 dB gain compression	f = 2400 MHz to f = 2500 MHz	-	90	-	W
f	frequency	P_L = 60 W	2400	-	2500	MHz
G_{flat}	gain flatness	P_L = 60 W; f = 2400 MHz to f = 2500 MHz	-	0.5	-	dB
RL_{in}	input return loss	P_L = 60 W; f = 2400 MHz to f = 2500 MHz	-	−25	−12	dB
η_D	drain efficiency	P_L = 60 W; f = 2450 MHz	38.5	41	-	%
$\alpha_{sup(H)}$	harmonic suppression	P_L = 300 W; f = 2450 MHz	-	30	-	dBc

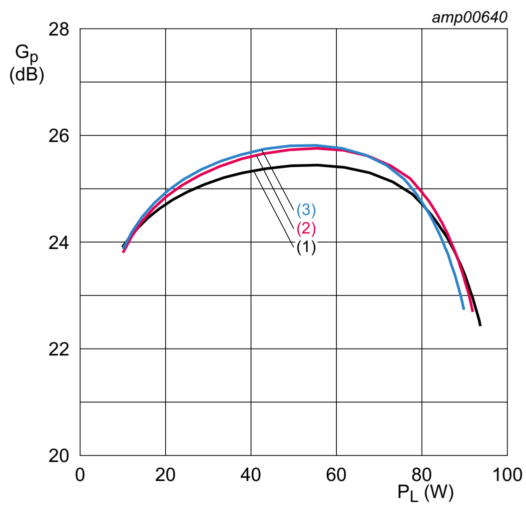
6.1 Ruggedness in class-AB operation

The BPC2425M7X60 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases with a time rate of 15 ms/degree under the following conditions: V_{DS} = 32 V; $I_{Dq1(A)}$ = $I_{Dq1(B)}$ = 25 mA; $I_{Dq2(A)}$ = $I_{Dq2(B)}$ = 50 mA; P_L = 60 W (CW); f = 2450 MHz; T_{mb} = 25 °C.

7. Test information

7.1 Graphical data

7.1.1 CW



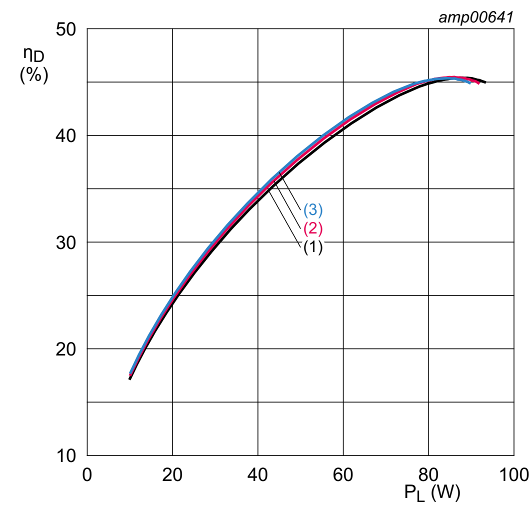
I_{Dq} = 2x25 + 2x50 mA; V_{DS} = 32 V; T_{mb} = 25 °C.

(1) f = 2400 MHz

(2) f = 2450 MHz

(3) f = 2500 MHz

Fig 3. Power gain as a function of output power; typical values



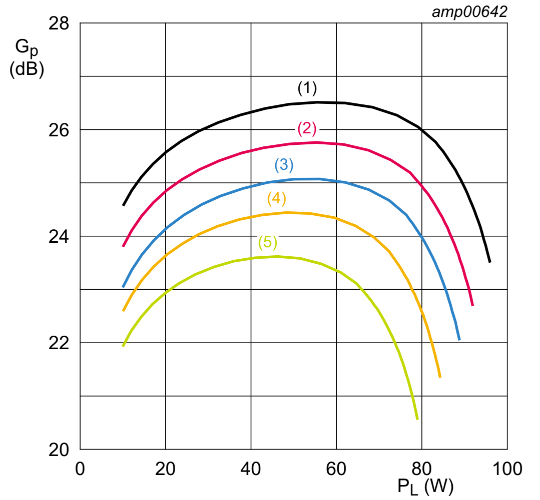
I_{Dq} = 2x25 + 2x50 mA; V_{DS} = 32 V; T_{mb} = 25 °C.

(1) f = 2400 MHz

(2) f = 2450 MHz

(3) f = 2500 MHz

Fig 4. Drain efficiency as a function of output power; typical values



I_{Dq} = 2x25 + 2x50 mA; V_{DS} = 32 V; f = 2450 MHz.

(1) T_{mb} = 5 °C

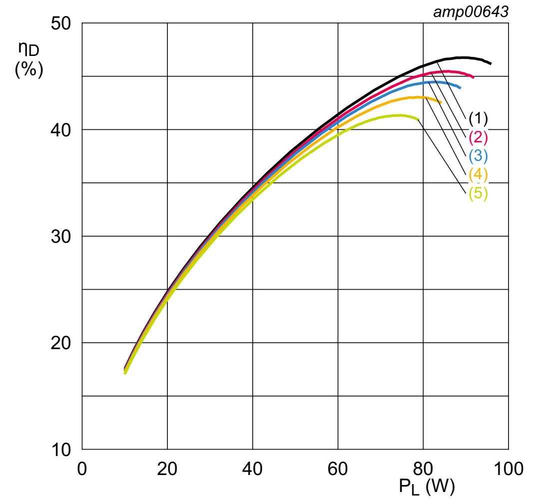
(2) T_{mb} = 25 °C

(3) T_{mb} = 40 °C

(4) T_{mb} = 60 °C

(5) T_{mb} = 85 °C

Fig 5. Power gain as a function of output power; typical values



I_{Dq} = 2x25 + 2x50 mA; V_{DS} = 32 V; f = 2450 MHz.

(1) T_{mb} = 5 °C

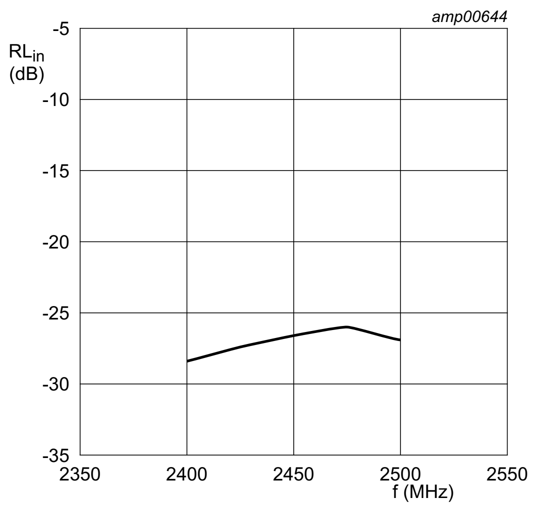
(2) T_{mb} = 25 °C

(3) T_{mb} = 40 °C

(4) T_{mb} = 60 °C

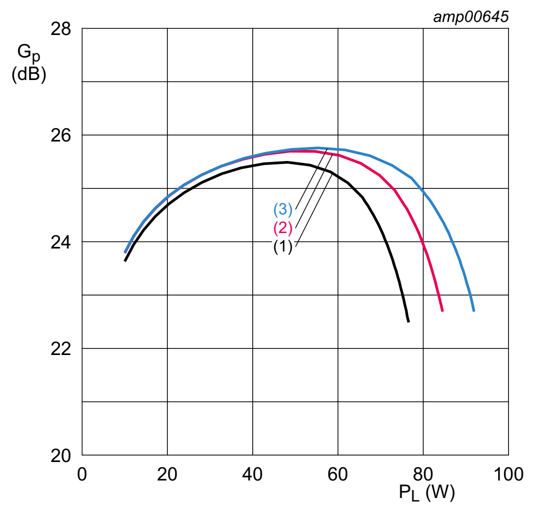
(5) T_{mb} = 85 °C

Fig 6. Drain efficiency as a function of output power; typical values



I_{Dq} = 2x25 + 2x50 mA; V_{DS} = 32 V; P_L = 60 W.

Fig 7. Input return loss as a function of frequency; typical values



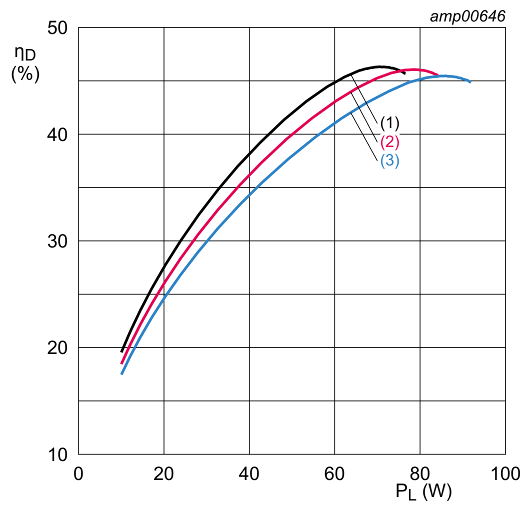
I_{Dq} = 2x25 + 2x50 mA; T_{mb} = 25 °C; f = 2450 MHz.

(1) V_{DS} = 28 V

(2) V_{DS} = 30 V

(3) V_{DS} = 32 V

Fig 8. Power gain as a function of output power; typical values



I_{Dq} = 2x25 + 2x50 mA; T_{mb} = 25 °C; f = 2450 MHz.

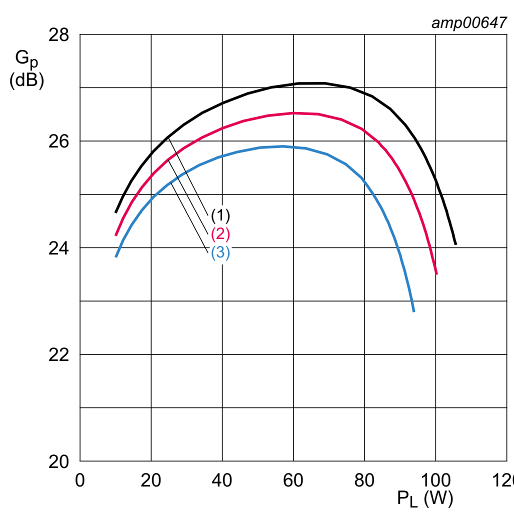
(1) V_{DS} = 28 V

(2) V_{DS} = 30 V

(3) V_{DS} = 32 V

Fig 9. Drain efficiency as a function of output power; typical values

7.1.2 CW pulsed



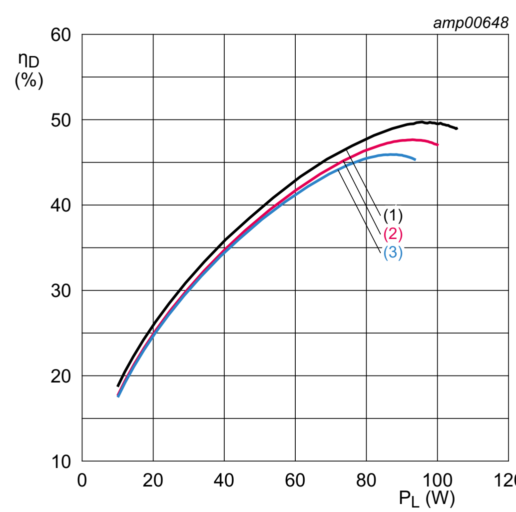
I_{Dq} = 2x25 + 2x50 mA; T_{mb} = 25 °C; f = 2450 MHz; V_{DS} = 32 V.

(1) t_p = 300 μs; δ = 10 %

(2) t_p = 300 μs; δ = 50 %

(3) t_p = 300 μs; δ = 90 %

Fig 10. Power gain as a function of output power; typical values



I_{Dq} = 2x25 + 2x50 mA; T_{mb} = 25 °C; f = 2450 MHz; V_{DS} = 32 V.

(1) t_p = 300 μs; δ = 10 %

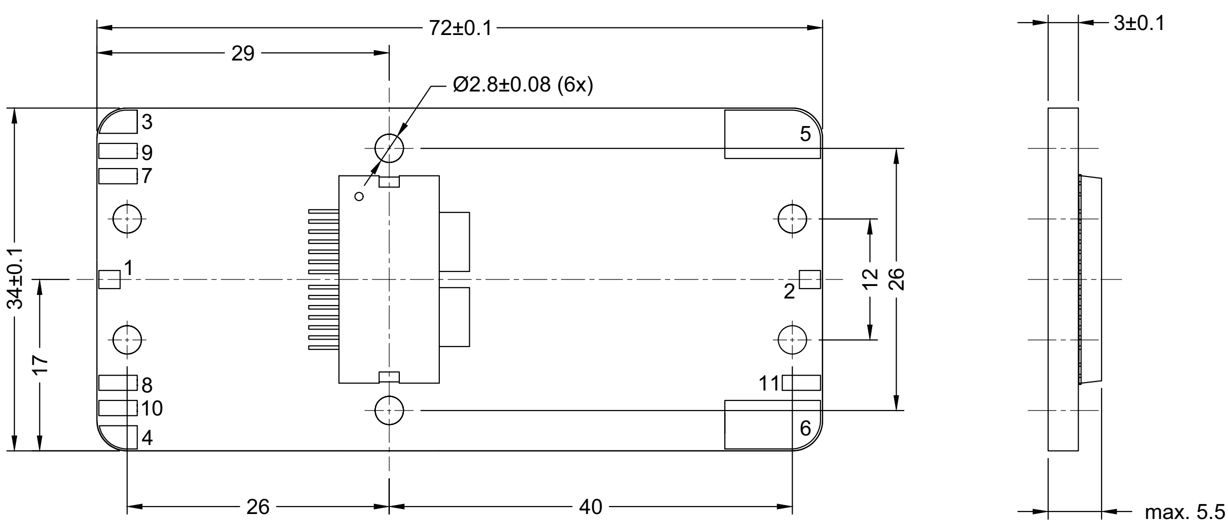
(2) t_p = 300 μs; δ = 50 %

(3) t_p = 300 μs; δ = 90 %

Fig 11. Drain efficiency as a function of output power; typical values

8. Package outline

Pallet; 6 mounting holes; 11 terminations BPC2425M7X60



Outline version	units in mm	Tolerances unless otherwise stated: Dimensions: ± 0.15 Angle: ± 1°	European projection	Issue date
BPC2425M7X60				2018-03-06

Fig 12. Package outline

9. Handling information

CAUTION	
	This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

Table 7. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C1 [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	1C [2]

[1] CDM classification C1 is granted to any part that passes after exposure to an ESD pulse of 250 V, but fails after exposure to an ESD pulse of 500 V.

[2] HBM classification 1C is granted to any part that passes after exposure to an ESD pulse of 1000 V, but fails after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 8. Abbreviations

Acronym	Description
CW	Continuous Wave
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
MTF	Median Time to Failure
RoHS	Restriction of Hazardous Substances
VSWR	Voltage Standing Wave Ratio

11. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BPC2425M7X60 v.1	20180329	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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14. Contents

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