Power LDMOS transistor

Rev. 3 — 1 September 2015

AMPLEON Product data sheet

1. Product profile

1.1 General description

75~W LDMOS power transistor for base station applications at frequencies from 1800 MHz to 2000 MHz.

Table 1.Typical performance

RF performance at $T_{case} = 25 \ ^{\circ}C$ in a common source class-AB production test circuit.

Mode of operation	f	\mathbf{V}_{DS}	P _{L(AV)}	Gp	η_{D}	ACPR _{400k}	ACPR _{600k}	EVM _{rms}
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)	(%)
CW	1930 to 1990	28	63	19	52	-	-	-
GSM EDGE	1930 to 1990	28	29.5	19	37.5	-61.5	-73	1.7

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical GSM EDGE performance at frequencies of 1930 MHz and 1990 MHz, a supply voltage of 28 V and an I_{Dq} of 550 mA:
 - Average output power = 29.5 W
 - ◆ Gain = 19 dB
 - Efficiency = 37.5 %
 - ACPR_{400k} = -61.5 dBc
 - ♦ ACPR_{600k} = -73 dBc
 - ◆ EVM_{rms} = 1.7 %
- Easy power control
- Integrated ESD protection
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (1800 MHz to 2000 MHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for GSM, GSM EDGE, W-CDMA and CDMA base stations and multi carrier applications in the 1800 MHz to 2000 MHz frequency range

2. Pinning information

Pin	Description		Simplified outline	Graphic symbol
BLF6G20	-75 (SOT502A)			
1	drain			
2	gate		$\int \int \frac{1}{2}$	1
3	source	<u>[1]</u>		
				3
BLF6G20	LS-75 (SOT502B)			
1	drain			
2	gate			1 لــــا
3	source	<u>[1]</u>		
				2 1 1
				sym112

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information						
Type number	number Package					
	Name	Description	Version			
BLF6G20-75	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A			
BLF6G20LS-75	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	18	А
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	225	°C

5. Thermal characteristics

Table 5.	Thermal characteristics				
Symbol	Parameter	Conditions	Туре	Тур	Unit
R _{th(j-case)}			BLF6G20-75	0.9	K/W
	junction to case	P _L = 29.5 W (CW)	BLF6G20LS-75	0.75	K/W

6. Characteristics

Table 6. Characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V_{GS} = 0 V; I _D = 0.5 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 100 mA	1.4	2	2.4	V
V_{GSq}	gate-source quiescent voltage	V_{DS} = 28 V; I_{D} = 600 mA	1.6	2.1	2.6	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	3	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	14.9	18.5	-	A
I _{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	300	nA
g _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 5 A	-	7	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 3.5 A$	-	0.15	0.235	Ω
C _{rs}	feedback capacitance	V _{GS} = 0 V; V _{DS} = 28 V; f = 1 MHz	-	1.6	-	pF

7. Application information

Table 7. Application information

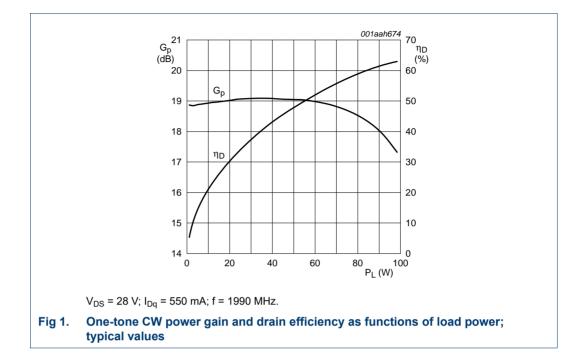
Mode of operation: GSM EDGE; f = 1930 MHz and 1990 MHz; RF performance at V_{DS} = 28 V; I_{Dg} = 550 mA; T_{case} = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Dy	, cuse - ,	· •				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P _{L(AV)}	average output power		-	29.5	-	W
G _p	power gain	P _{L(AV)} = 29.5 W	17.5	19	-	dB
RL _{in}	input return loss	P _{L(AV)} = 29.5 W	-	-10	-5.5	dB
η_D	drain efficiency	P _{L(AV)} = 29.5 W	33.5	37.5	-	%
ACPR _{400k}	adjacent channel power ratio (400 kHz)	P _{L(AV)} = 29.5 W	-	-61.5	-59.5	dBc
ACPR _{600k}	adjacent channel power ratio (600 kHz)	P _{L(AV)} = 29.5 W	-	-73	-69.5	dBc
EVM _{rms}	RMS EDGE signal distortion error	P _{L(AV)} = 29.5 W	-	1.7	3	%
EVMM	peak EDGE signal distortion error	P _{L(AV)} = 29.5 W	-	4.8	10	%

7.1 Ruggedness in class-AB operation

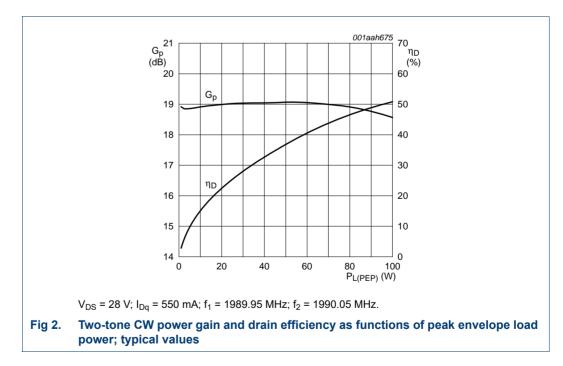
The BLF6G20-75 and BLF6G20LS-75 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 550 mA; P_L = 75 W (CW); f = 1990 MHz.

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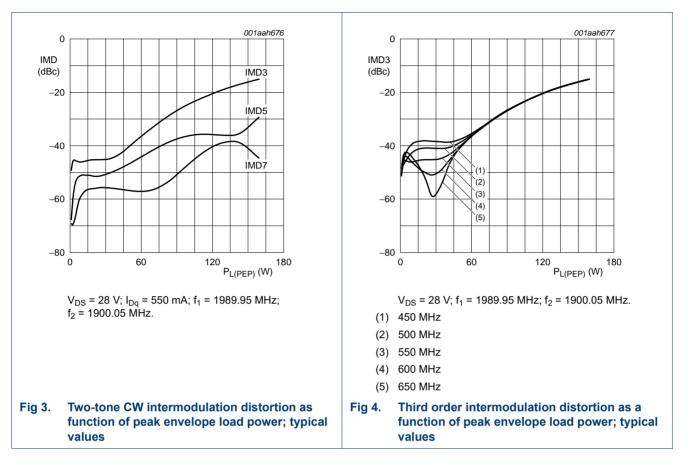
7.2 One-tone CW

7.3 Two-tone CW

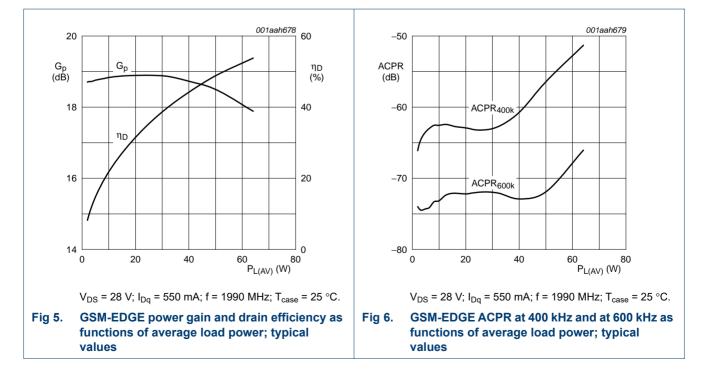


BLF6G20-75; BLF6G20LS-75

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7.4 GSM-EDGE



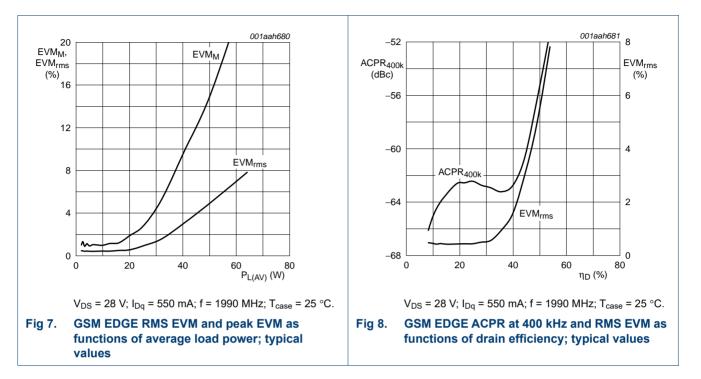
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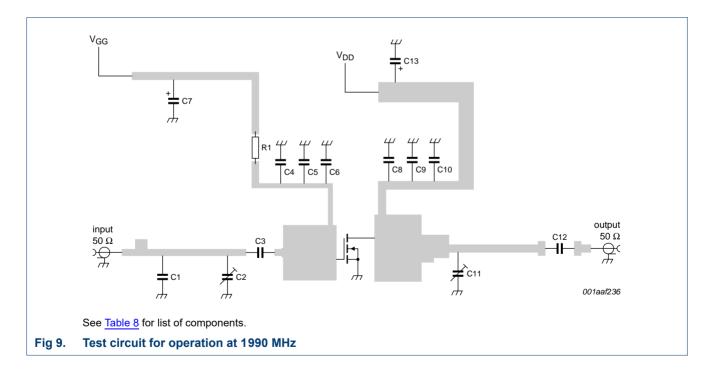
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BLF6G20-75; BLF6G20LS-75

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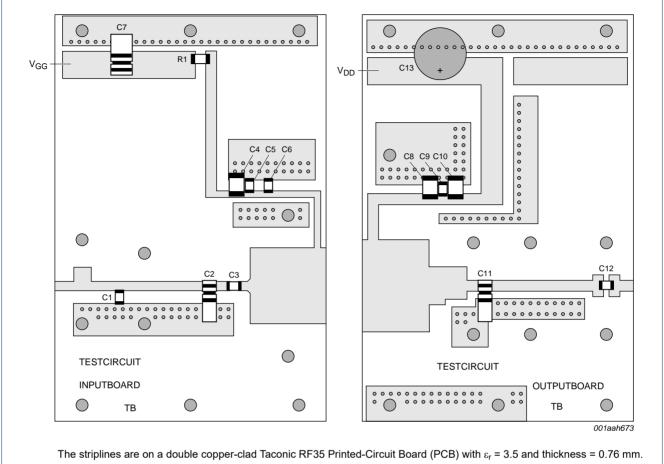


8. Test information



BLF6G20-75; BLF6G20LS-75

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See Table 8 for list of components.

Fig 10. Component layout for 1990 MHz test circuit

Table 8. List of components (see Figure 9 and Figure 10)

Component	Description	Value	Remarks
C1	multilayer ceramic chip capacitor	1 pF	<u>[1]</u>
C2, C11	gigahertz trimmer	0.6 pF to 4.5 pF	Temex AT SM270 or equivalent
C3, C6, C9	multilayer ceramic chip capacitor	12 pF	<u>[1]</u>
C4, C8, C10	multilayer ceramic chip capacitor	10 μF; 50 V	TDK C5750X7R1H106M or equivalent
C5	multilayer ceramic chip capacitor	1.5 μF; 50 V	TDK C3225X7R1H155M or equivalent
C7	tantalum capacitor	10 μF; 50 V	Kemet T491 series or equivalent
C12	multilayer ceramic chip capacitor	12 pF	<u>[1]</u>
C13	electrolytic capacitor	220 μF; 50 V	
R1	Philips chip resistor	5.6 Ω; 1206	

[1] American Technical Ceramics type 100B or capacitor of same quality.

BLF6G20-75; BLF6G20LS-75

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9. Package outline

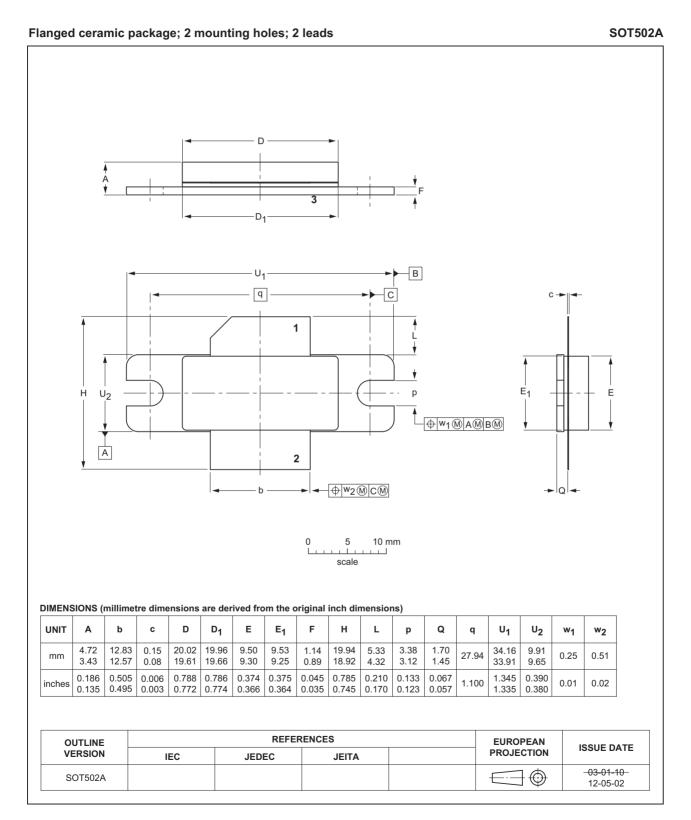


Fig 11. Package outline SOT502A

BLF6G20-75_BLF6G20LS-75#3

BLF6G20-75; BLF6G20LS-75

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SOT502B

Earless flanged ceramic package; 2 leads

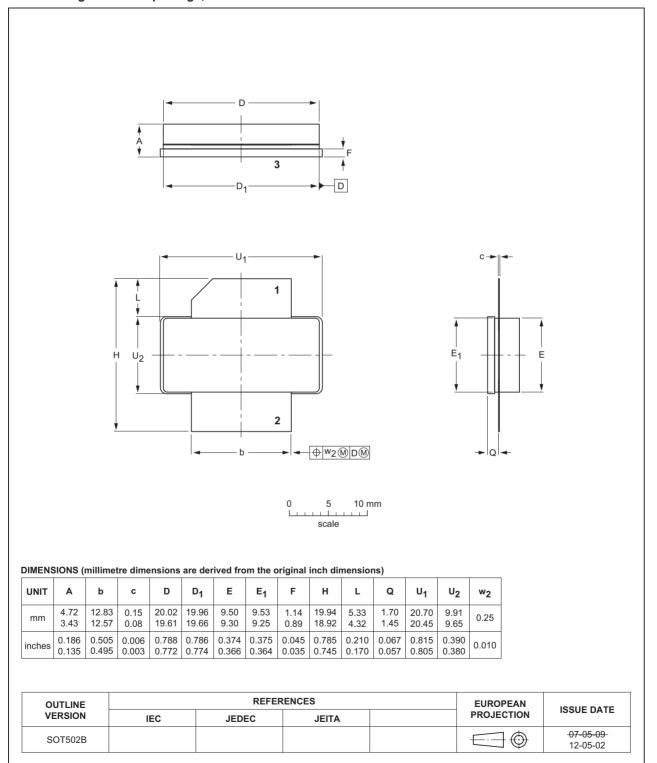


Fig 12. Package outline SOT502B

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Product data sheet

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10. Abbreviations

Table 9.	Abbreviations
Acronym	Description
CDMA	Code Division Multiple Access
CW	Continuous Wave
EDGE	Enhanced Data rates for GSM Evolution
EVM	Error Vector Magnitude
GSM	Global System for Mobile communications
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
LDMOST	Laterally Diffused Metal-Oxide Semiconductor Transistor
RF	Radio Frequency
RMS	Root Mean Square
VSWR	Voltage Standing-Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

11. Revision history

Table 10.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BLF6G20-75_BLF6G20LS-75#3	20150901	Product data sheet	-	BLF6G20-75_1BLF6G20LS -75_2	
Modifications:	 The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. Legal texts have been adapted to the new company name where appropriate. 				
BLF6G20-75_1BLF6G20LS-75_ 2	20090209	Product data sheet	-	BLF6G20-75_1BLF6G20LS -75_1	
BLF6G20-75_1	20080306	Preliminary data sheet	-	-	
BLF6G20LS-75_1	20080218	Preliminary data sheet	-	-	

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12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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