BLF6G10-135RN; BLF6G10LS-135RN Power LDMOS transistor

Rev. 3 — 1 September 2015



Product profile 1.

1.1 General description

135 W LDMOS power transistor for base station applications at frequencies from 700 MHz to 1000 MHz.

Table 1. **Typical performance**

Typical RF performance at T_{case} = 25 °C in a class-AB production test circuit.

Mode of operation	f	V _{DS}	P _{L(AV)}	Gp	η_D	ACPR	
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)	
2-carrier W-CDMA	869 to 894	28	26.5	21.0	28.0	-39 <mark>[1]</mark>	

[1] Test signal: 3GPP; test model 1; 64 DPCH; PAR = 7.5 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical 2-carrier W-CDMA performance at frequencies of 869 MHz and 894 MHz, a supply voltage of 28 V and an I_{Dq} of 950 mA:
 - Average output power = 26.5 W
 - Power gain = 21.0 dB
 - Efficiency = 28.0 %
 - ◆ ACPR = -39 dBc
- Easy power control
- Integrated ESD protection
- Enhanced ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (700 MHz to 1000 MHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding restriction of hazardous substances (RoHS)

1.3 Applications

RF power amplifiers for GSM, GSM EDGE, W-CDMA and CDMA base stations and multi carrier applications in the 700 MHz to 1000 MHz frequency range

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
BLF6G1	0-135RN (SOT502A)		
1	drain		
2	gate		1 لــــا
3	source		
		2	3 sym112
BLF6G1	0LS-135RN (SOT502B)		
1	drain		_
2	gate		1 لــــا
3	source	[1] 3	
		2	2 1 1
			sym1

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information					
Packag	Package				
Name	Description	Version			
-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A			
-	earless flanged LDMOST ceramic package; 2 leads	SOT502B			
	Packag Name	Package Name Description - flanged LDMOST ceramic package; 2 mounting holes; 2 leads			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	32	А
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	225	°C

5. Thermal characteristics

Table 5.	Thermal characteristics				
Symbol	Parameter	Conditions	Туре	Тур	Unit
R _{th(j-case)}		T_{case} = 80 °C; P_L = 25 W	BLF6G10-135RN	0.68	K/W
	junction to case		BLF6G10LS-135RN	0.56	K/W

6. Characteristics

Table 6. Characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V_{GS} = 0 V; I _D = 0.8 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 180 mA	1.4	1.9	2.4	V
V _{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _D = 950 mA	1.6	2.1	2.6	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	3	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	24	32	-	A
I _{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	300	nA
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 9 A	7	13	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ I _D = 6.3 A	-	0.1	-	Ω
C _{rs}	feedback capacitance	V _{GS} = 0 V; V _{DS} = 28 V; f = 1 MHz	-	2.0	-	pF

7. Application information

Table 7. Application information

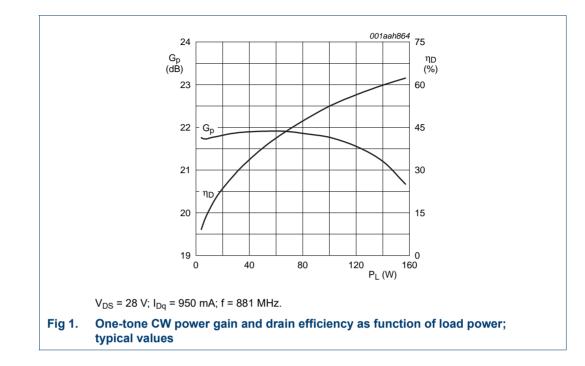
Mode of operation: 2-carrier W-CDMA; PAR 7.5 dB at 0.01 % probability on CCDF; 3GPP test model 1; 1-64 PDPCH; $f_1 = 871.5$ MHz; $f_2 = 876.5$ MHz; $f_3 = 886.5$ MHz; $f_4 = 891.5$ MHz; RF performance at $V_{DS} = 28$ V; $I_{Dq} = 950$ mA; $T_{case} = 25$ °C; unless otherwise specified; in a class-AB production test circuit.

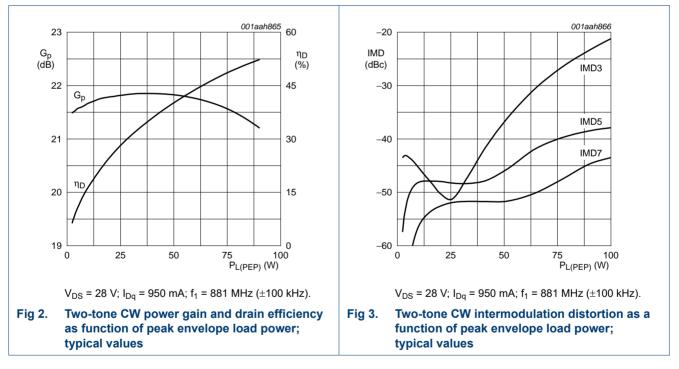
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P _{L(AV)}	average output power		-	26.5	-	W
G _p	power gain	P _{L(AV)} = 26.5 W	20.0	21.0	-	dB
RLin	input return loss	P _{L(AV)} = 26.5 W	-	-10.0	-6.5	dB
η_D	drain efficiency	P _{L(AV)} = 26.5 W	26.0	28.0	-	%
ACPR	adjacent channel power ratio	P _{L(AV)} = 26.5 W	-	-39	-36.5	dBc

7.1 Ruggedness in class-AB operation

The BLF6G10-135RN and BLF6G10LS-135RN are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 950 mA; P_L = 135 W; f = 894 MHz.

Power LDMOS transistor

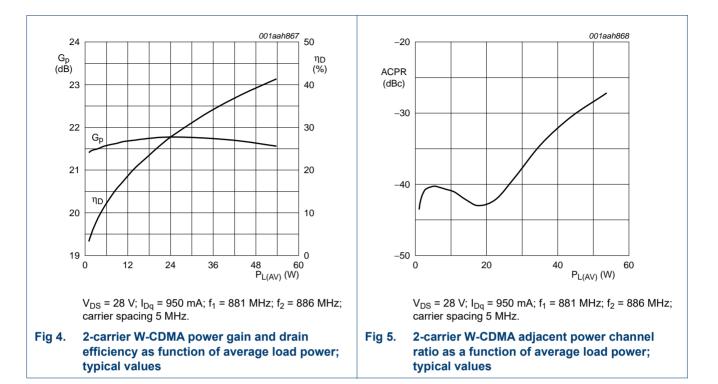




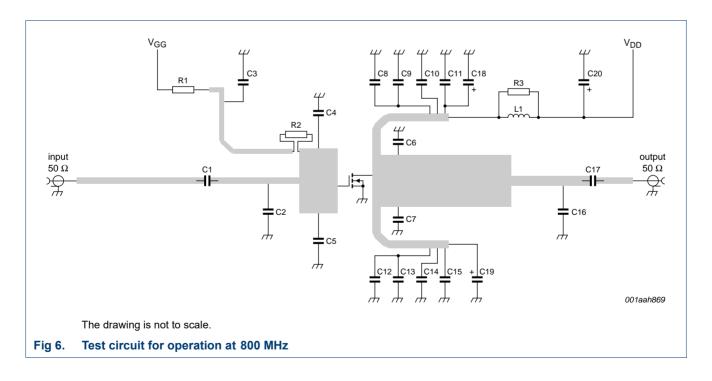
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BLF6G10(LS)-135RN

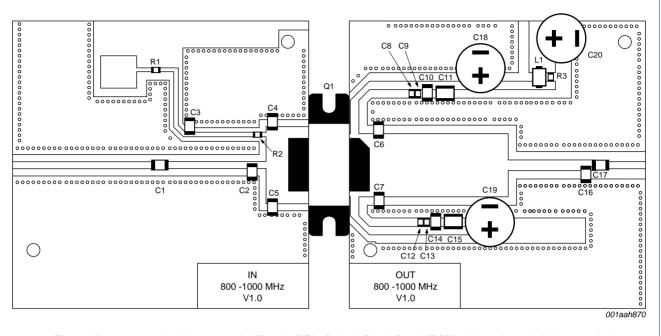
Power LDMOS transistor



8. Test information



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The striplines are on a double copper-clad Taconic RF35 Printed-Circuit Board (PCB) with ε_r = 3.5 and thickness = 0.76 mm. See <u>Table 8</u> for list of components. The drawing is not to scale.

Fig 7. Component layout

Table 8. List of components

See Figure 6 and Figure 7.

Component	Description	Value		Remarks
C1, C3, C10, C14, C17	multilayer ceramic chip capacitor	68 pF	[1]	solder vertically
C2, C4, C5	multilayer ceramic chip capacitor	8.2 pF	[1]	solder vertically
C6, C7	multilayer ceramic chip capacitor	10 pF	[1]	solder vertically
C8, C9, C12, C13	electrolytic capacitor	100 nF		Vishay or capacitor of same quality.
C11, C15	multilayer ceramic chip capacitor	4.7 μF; 50 V	[2]	
C16	multilayer ceramic chip capacitor	3.0 pF	[1]	solder vertically
C18, C19, C20	electrolytic capacitor	220 μF; 63 V		
L1	ferrite SMD bead			Ferroxcube BDS 3/3/4.6-4S2 or equivalent
Q1	BLF6G10LS-135RN			
R1, R2, R3	SMD resistor	9.1 Ω; 0.1 W		

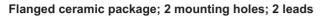
[1] American Technical Ceramics type 100B or capacitor of same quality.

[2] TDK or capacitor of same quality.

Power LDMOS transistor

SOT502A

9. Package outline



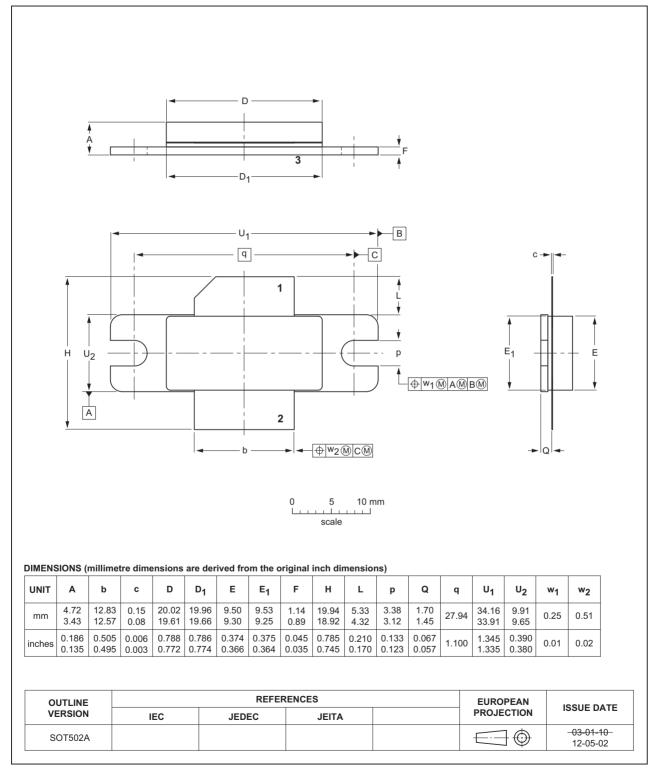


Fig 8. Package outline SOT502A

BLF6G10-135RN_10LS-135RN#3

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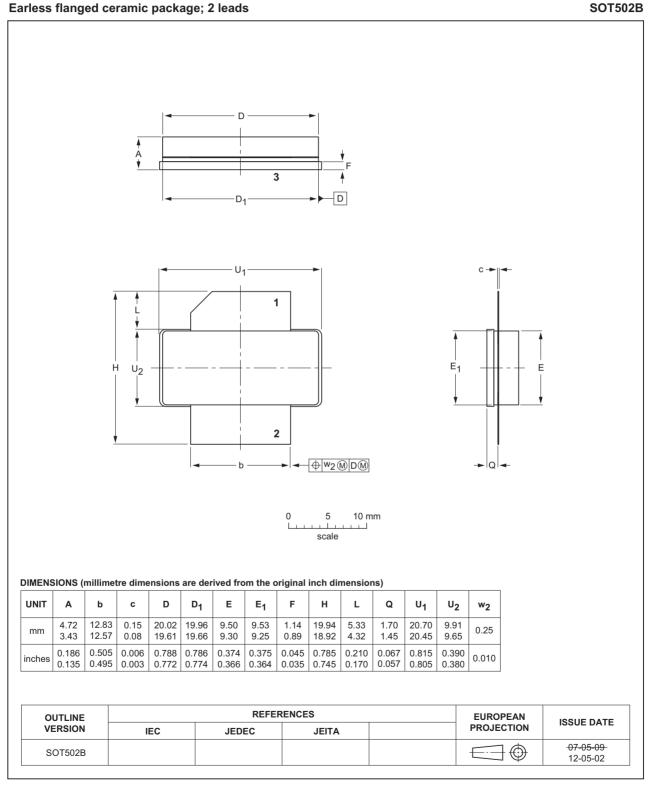


Fig 9. Package outline SOT502B

BLF6G10-135RN_10LS-135RN#3

10. Abbreviations

AcronymDescription3GPPThird Generation Partnership ProjectCCDFComplementary Cumulative Distribution FunctionCDMACode Division Multiple AccessCWContinuous WaveDPCHDedicated Physical CHannel	
CCDFComplementary Cumulative Distribution FunctionCDMACode Division Multiple AccessCWContinuous Wave	
CDMACode Division Multiple AccessCWContinuous Wave	
CW Continuous Wave	
DPCH Dedicated Physical CHannel	
EDGE Enhanced Data rates for GSM Evolution	
GSM Global System for Mobile communications	
LDMOS Laterally Diffused Metal-Oxide Semiconductor	
LDMOST Laterally Diffused Metal-Oxide Semiconductor Tran	isistor
PAR Peak-to-Average power Ratio	
PDPCH transmission Power of the Dedicated Physical CHa	innel
RF Radio Frequency	
SMD Surface Mounted Device	
VSWR Voltage Standing-Wave Ratio	
W-CDMA Wideband Code Division Multiple Access	

11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF6G10-135RN_10LS-135RN#3	20150901	Product data sheet	-	BLF6G10-135RN_10LS-135RN_2
Modifications:	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.			
	 Legal texts 	s have been adapted	I to the new comp	pany name where appropriate.
BLF6G10-135RN_10LS-135RN_2	20100121	Product data sheet	-	BLF6G10-135RN_10LS-135RN_1
BLF6G10-135RN_10LS-135RN_1	20090210	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.ampleon.com.

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