# BLF2425M8L140; BLF2425M8LS140 Power LDMOS transistor Rev. 2 – 1 September 2015

AMPLEON Product data sheet

#### **Product profile** 1.

## 1.1 General description

140 W LDMOS power transistor for Industrial, Scientific and Medical (ISM) applications at frequencies from 2400 MHz to 2500 MHz.

The BLF2425M8L140 and BLF2425M8LS140 are designed for high-power CW applications and are assembled in high performance ceramic packages, available in eared and earless versions

#### **Typical performance** Table 1.

Typical RF performance at  $T_{case} = 25$  °C;  $I_{Dq} = 1300$  mA in a common source class-AB production test circuit.

Test signal	f	V <sub>DS</sub>	P <sub>L(AV)</sub>	Gp	η <sub>D</sub>
	(MHz)	(V)	(W)	(dB)	(%)
CW	2450	28	140	19	56

## 1.2 Features and benefits

- High efficiency
- High power gain
- Excellent ruggedness
- Excellent thermal stability
- Integrated ESD protection
- Designed for broadband operation (2400 MHz to 2500 MHz)
- Internally matched
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

## 1.3 Applications

Industrial, scientific and medical applications in the frequency range from 2400 MHz to 2500 MHz

**Power LDMOS transistor** 

# 2. Pinning information

Pin	Description		Simplified outline	Graphic symbol
BLF2425	M8L140 (SOT502A)			
1	drain			
2	gate			1 لــــا
3	source	<u>[1]</u>		
				- ·   3 sym112
				Synniz
BLF2425	M8LS140 (SOT502B)			
1	drain			4
2	gate			نے.
3	source	<u>[1]</u>		
				-     3
				sym112

- - - -

# 3. Ordering information

Table 3. Ordering information				
Type number	Packag	je		
	Name	Description	Version	
BLF2425M8L140	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT502A	
BLF2425M8LS140	-	earless flanged ceramic package; 2 leads	SOT502B	

# 4. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage			-	65	V
V <sub>GS</sub>	gate-source voltage			-0.5	+13	V
T <sub>stg</sub>	storage temperature			-65	-	°C
Tj	junction temperature		<u>[1]</u>	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability

# 5. Thermal characteristics

Т	Table 5.	Thermal characteristics			
	Symbol	Parameter	Conditions	Тур	Unit
ł	R <sub>th(j-c)</sub>	thermal resistance from junction to case	T <sub>case</sub> = 80 °C; P <sub>L</sub> = 125 W	0.28	K/W

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## 6. Characteristics

#### Table 6.DC characteristics

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit		
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS}$ = 0 V; I <sub>D</sub> = 2.16 mA	65	-	-	V		
V <sub>GS(th)</sub>	gate-source threshold voltage	$V_{DS}$ = 10 V; I <sub>D</sub> = 216 mA	1.5	1.9	2.3	V		
I <sub>DSS</sub>	drain leakage current	$V_{GS}$ = 0 V; $V_{DS}$ = 28 V	-	-	5	μA		
I <sub>DSX</sub>	drain cut-off current	$\label{eq:VGS} \begin{array}{l} V_{\mathrm{GS}} = V_{\mathrm{GS}(\mathrm{th})} + 3.75 \; V; \\ V_{\mathrm{DS}} = 10 \; V \end{array}$	-	41	-	A		
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-	-	500	nA		
g <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 10.8 A	-	16	-	S		
R <sub>DS(on)</sub>	drain-source on-state resistance	V <sub>GS</sub> = V <sub>GS(th)</sub> + 3.75 V; I <sub>D</sub> = 7.56 A	-	69	-	mΩ		

#### Table 7. RF characteristics

Test signal: CW; f = 2450 MHz;  $V_{DS} = 28 \text{ V}$ ;  $I_{Dq} = 1300 \text{ mA}$ ;  $T_{case} = 25 \text{ }^{\circ}\text{C}$  unless otherwise specified in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G <sub>p</sub>	power gain	P <sub>L</sub> = 140 W	17.5	19	-	dB
RLin	input return loss	P <sub>L</sub> = 140 W	-	-16	-8	dB
$\eta_D$	drain efficiency	P <sub>L</sub> = 140 W	51	56	-	%

# 7. Test information

#### 7.1 Ruggedness in class-AB operation

The BLF2425M8L140 and BLF2425M8LS140 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 28 V;  $I_{Dg}$  = 1300 mA;  $P_L$  = 140 W (CW); f = 2450 MHz.

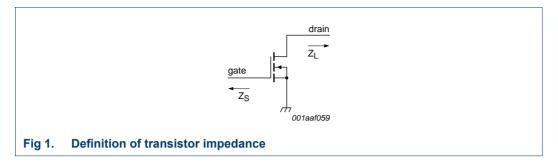
### 7.2 Impedance information

#### Table 8. Typical impedance

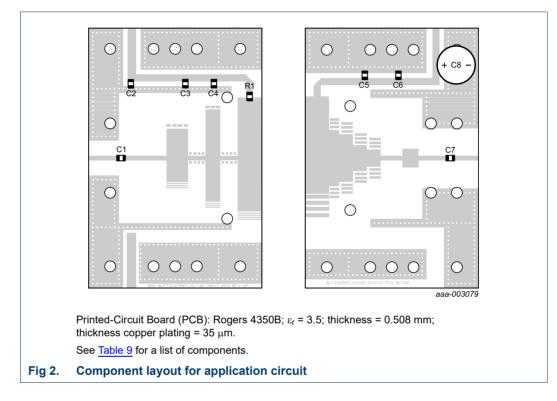
Measured load-pull data. Typical values unless otherwise specified.  $I_{Dq} = 1300 \text{ mA}$ ;  $V_{DS} = 28 \text{ V}$ .  $Z_S$  and  $Z_L$  defined in Figure 1.

f	Z <sub>S</sub>	ZL
(MHz)	(Ω)	(Ω)
2400	3.7 – 5.4j	1.3 – 1.5j
2450	6.9 – 5.0j	1.5 – 1.6j
2500	8.7 – 2.0j	1.5 – 1.6j

BLF2425M8L(S)140



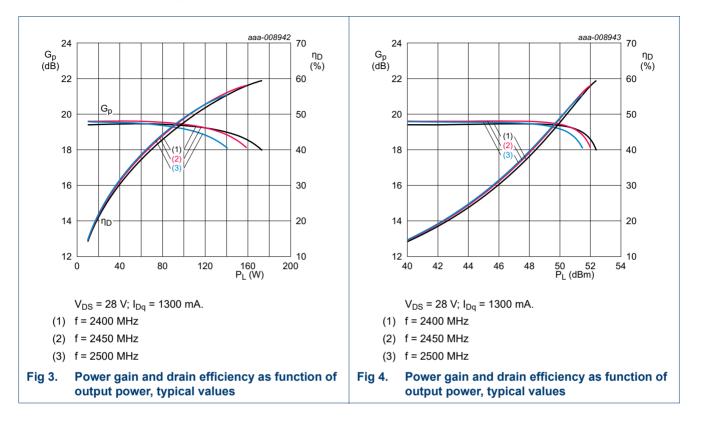
## 7.3 Circuit information



# Table 9.List of componentsFor test circuit see Figure 2.

Component	Description	Value	Remarks
C1, C4, C5	multilayer ceramic chip capacitor	15 pF	ATC100B
C2, C6	multilayer ceramic chip capacitor	10 μF, 50 V	Murata
C3	multilayer ceramic chip capacitor	100 nF	Murata
C7	multilayer ceramic chip capacitor	62 pF	ATC100B
C8	electrolytic capacitor	22 μF, 63 V	
R1	resistor	10 Ω	SMD 0805; Bourns

**Power LDMOS transistor** 

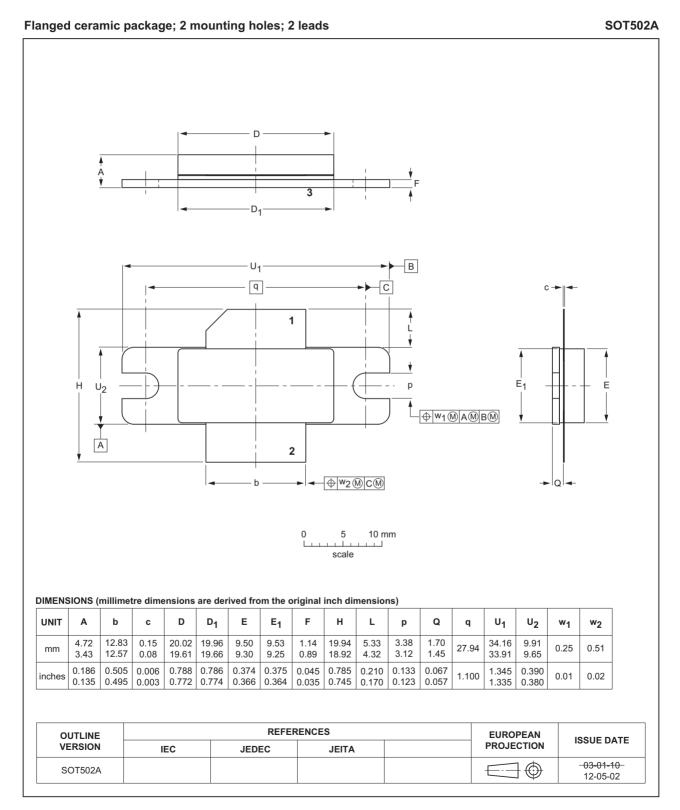


## 7.4 Graphical data

BLF2425M8L(S)140

**Power LDMOS transistor** 

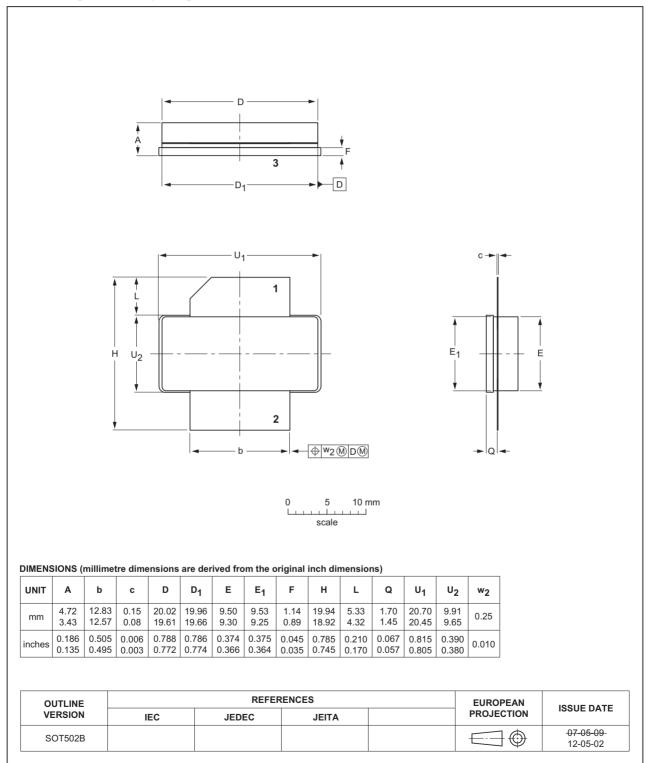
# 8. Package outline



#### Fig 5. Package outline SOT502A

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#### Earless flanged ceramic package; 2 leads



#### Fig 6. Package outline SOT502B

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SOT502B

# 9. Handling information

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

# **10. Abbreviations**

Table 10. Abbreviations			
Acronym	Description		
CW	Continuous Wave		
ESD	ElectroStatic Discharge		
LDMOS	Laterally Diffused Metal Oxide Semiconductor		
SMD	Surface Mounted Device		
VSWR	Voltage Standing Wave Ratio		

# 11. Revision history

#### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF2425M8L140_2425M8LS140 v.2	20150901	Product data sheet		BLF2425M8L140_ 2425M8LS140 v.1
Modifications:	<ul> <li>The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>			
BLF2425M8L140_2425M8LS140 v.1	20130827	Product data sheet	-	-

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Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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