Avionics LDMOS transistor

Rev. 9 — 1 September 2015

AMPLEON Product data sheet

1. Product profile

1.1 General description

200 W LDMOS avionics power transistor for transmitter applications at frequencies from 1030 MHz to 1090 MHz.

Table 1.Typical performance

RF performance at $T_h = 25$ °C in a common source class-AB test circuit; $I_{Dq} = 150$ mA; typical values.

Mode of operation	Conditions	V _{DS} (V)	P _L (W)	G _p (dB)	η _D (%)	t _r (ns)	t _f (ns)
Pulsed class-AB: 1030 MHz to 1090 MHz	t_p = 50 μ s; δ = 2 %	36	200	15	50	35	6
	t _p = 128 μs; δ = 2 %	36	250	14	50	35	6
	t _p = 340 μs; δ = 1 %	36	250	14	50	35	6

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical pulsed class-AB performance at a frequencies from 1030 MHz to 1090 MHz, a supply voltage of 36 V and an I_{Dq} of 150 mA:
 - ◆ Load power ≥ 200 W
 - ♦ Gain ≥ 13 dB
 - Efficiency \geq 45 %
 - Rise time \leq 50 ns
 - Fall time \leq 50 ns
- High power gain
- Easy power control
- Excellent ruggedness
- Source on mounting flange eliminates DC isolators, reducing common mode inductance

1.3 Applications

Avionics transmitter applications in the 1030 MHz to 1090 MHz frequency range.

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2. Pinning information

Pin	Description	Simplified outline Symbol
BLA1011	-200 (SOT502A)	
1	drain	
2	gate	
3	source	
		3 sym039
BLA1011	S-200 (SOT502B)	
1	drain	
2	gate	
3	source	
		3 sym039

[1] Connected to flange

3. Ordering information

Table 3.Ordering information

Type number	Packag	Package					
	Name	Description	Version				
BLA1011-200	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A				
BLA1011S-200	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B				

4. Limiting values

4.1 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	75	V
V _{GS}	gate-source voltage		-	±22	V
P _{tot}	total power dissipation	T_h \leq 25 °C; t_p = 50 $\mu s;$ δ = 2 %	-	700	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
Z _{th(j-h)}	thermal impedance from junction to heatsink	T _h = 25 °C	<u>1</u> 0.15	K/W

[1] Thermal resistance is determined under RF operating conditions; t_p = 50 µs, δ = 10 %.

6. Characteristics

Table 6. Characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V _{GS} = 0 V; I _D = 3 mA	75	-	-	V
V _{GS(th)}	gate-source threshold voltage	V_{DS} = 10 V; I _D = 300 mA	4	-	5	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 36 V	-	-	1	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 9 V;$ $V_{DS} = 10 V$	45	-	-	A
I _{GSS}	gate leakage current	V_{GS} = ±20 V; V_{DS} = 0 V	-	-	1	μA
g _{fs}	transfer conductance	V _{DS} = 10 V; I _D = 10 A	-	9	-	S
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 9 V; I _D = 10 A	-	60	-	mΩ

7. Application information

Table 7. Application information

RF performance in a common source pulsed class-AB circuit; ($t_p = 50 \ \mu s$; $\delta = 2 \ \%$); $f = 1030 \ MHz$ and 1090 MHz; $T_h = 25 \ ^{\circ}C$; $Z_{th(mb-h)} = 0.15 \ K/W$; $I_{Dq} = 150 \ mA$; unless otherwise specified.

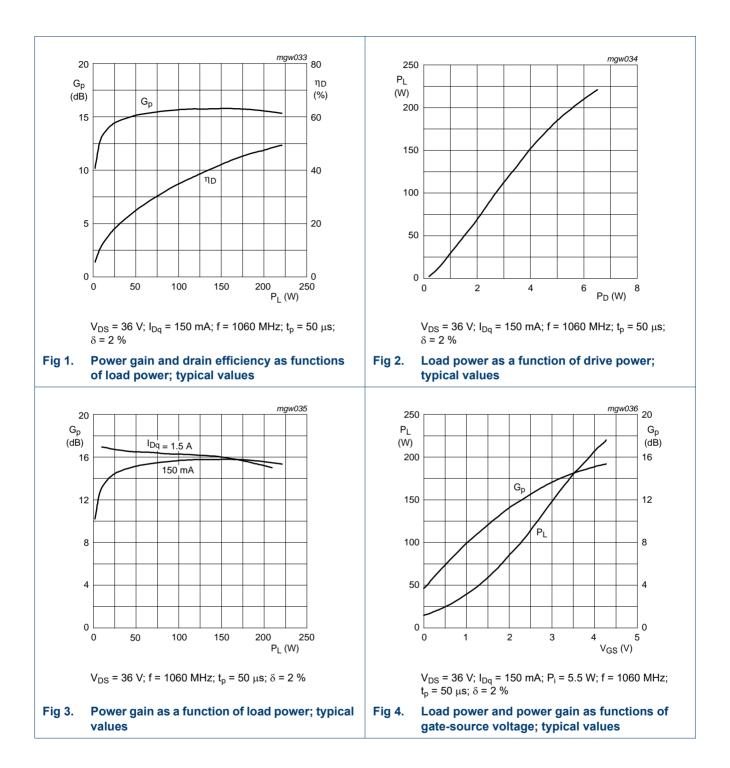
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DS}	drain-source voltage		-	36	-	V
PL	load power	t_p = 50 μ s; δ = 2 %	-	200		W
G _p	power gain	P _L = 200 W	13	-		dB
η_D	drain efficiency	t_p = 50 μ s; δ = 2 %	45	-		%
t _r	rise time		-	-	50	ns
t _f	fall time		-	-	50	ns

7.1 Ruggedness in class-AB operation

The BLA1011-200 and BLA1011S-200 are capable of withstanding a load mismatch corresponding to VSWR = 5 : 1 through all phases under the following conditions: V_{DS} = 36 V; f = 1030 MHz to 1090 MHz at rated load power.

BLA1011-200; BLA1011S-200

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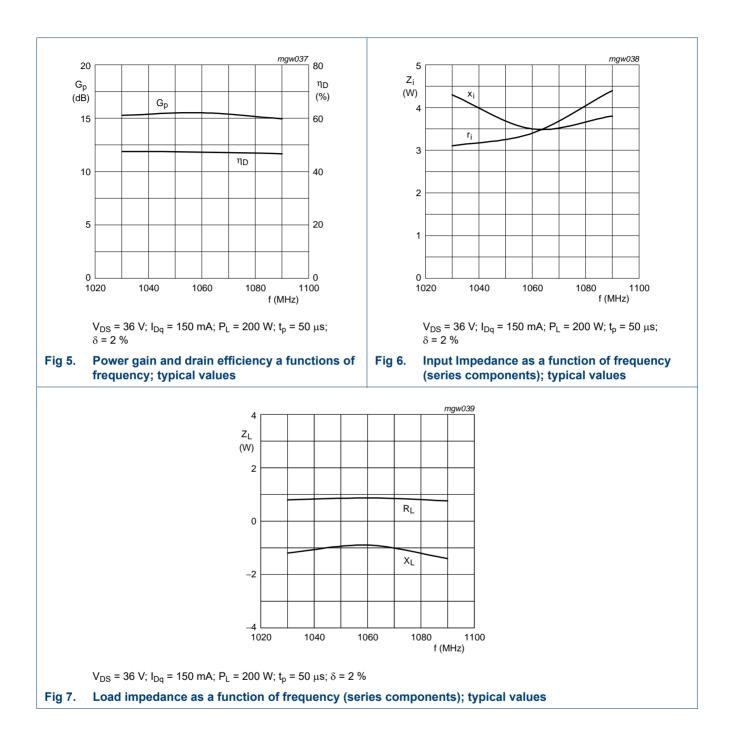


BLA1011-200; BLA1011S-200#9

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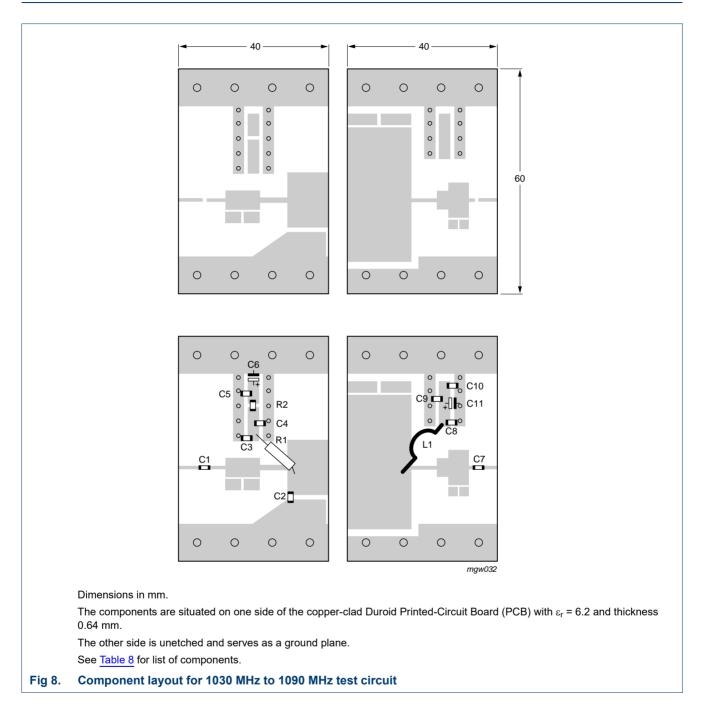
BLA1011-200; BLA1011S-200

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8. Test information



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Component	Description	Valu	e Dimens	sions
C1	multilayer ceramic chip capacitor	[<u>1]</u> 39 p	F	
C2	multilayer ceramic chip capacitor	[2] 4.3 p	ρF	
C3	multilayer ceramic chip capacitor	[<u>1]</u> 11 p	F	
C4, C7	multilayer ceramic chip capacitor	<mark>[1]</mark> 62 p	F	
C5	multilayer ceramic chip capacitor	<u>[1]</u> 100	pF	
C6	electrolytic capacitor	47 μ	F; 20 V	
C8	multilayer ceramic chip capacitor	[2] 20 p	F	
C9	multilayer ceramic chip capacitor	<mark>[1]</mark> 47 p	F	
C10	multilayer ceramic chip capacitor	<u>3</u> 1.2 r	۱F	
C11	electrolytic capacitor	47 μ	F; 63V	
L1	Ω -shaped enamelled 1 mm copper wire		length =	= 38 mm
R1	metal film resistor	301	Ω	
R2	SMD 0508 resistor	18 <u>C</u>	2	

Table 8. List of components (see Figure 8)

[1] American Technical Ceramics type 100A or capacitor of same quality.

[2] American Technical Ceramics type 100B or capacitor of same quality.

[3] American Technical Ceramics type 700 or capacitor of same quality.

BLA1011-200; BLA1011S-200

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9. Package outline

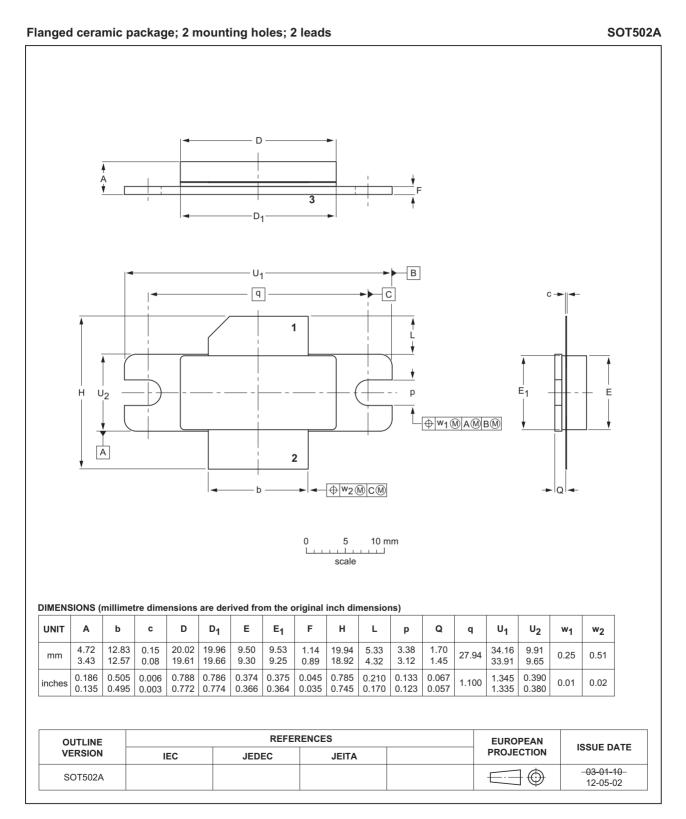


Fig 9. Package outline SOT502A

BLA1011-200; BLA1011S-200

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SOT502B

Earless flanged ceramic package; 2 leads

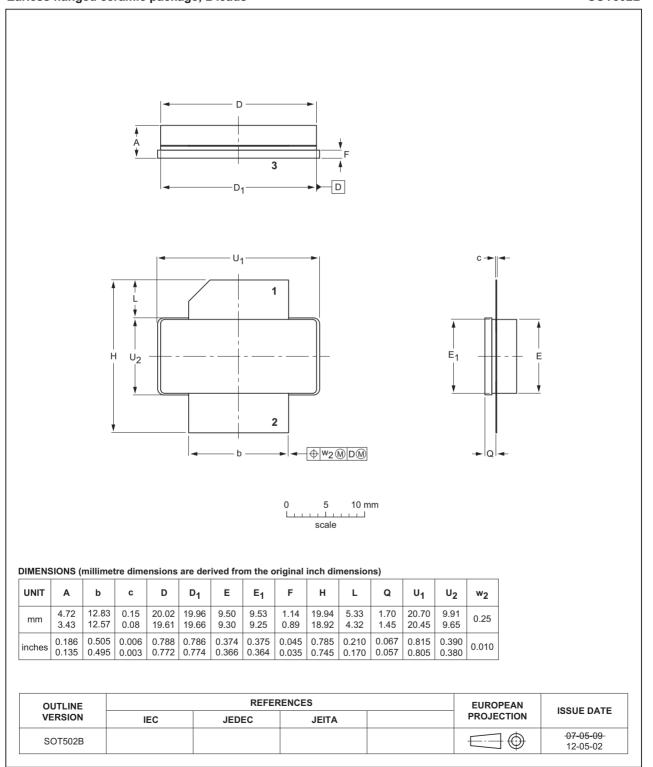


Fig 10. Package outline SOT502B

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10. Abbreviations

Table 9.	Abbreviations
Acronym	Description
I _{Dq}	quiescent drain current
LDMOS	Laterally Diffused Metal Oxide Semiconductor
RF	Radio Frequency
SMD	Surface Mount Device
VSWR	Voltage Standing Wave Ratio

BLA1011-200; BLA1011S-200#9

Product data sheet

11. Revision history

Table 10.Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BLA1011-200_BLA1 011S-200#9	20150901	Product data sheet	-	-	BLA1011-200_8
Modifications:	The form of Ample	nat of this document has b eon.	een redesigned to	comply with the n	ew identity guidelines
	 Legal te 	exts have been adapted to	the new company	name where appro	opriate.
BLA1011-200_BLA1 011S-200_8	20051026	Product data sheet	-	-	BLA1011-200_7
BLA1011-200_7	20031111	Product specification	-	9397 750 12246	BLA1011-200_6
BLA1011-200_6	20020318	Product specification	-	9397 750 09414	BLA1011-200_5
BLA1011-200_5	20010515	Product specification	-	9397 750 08376	BLA1011-200_4
BLA1011-200_4	20010417	Product specification	-	9397 750 08139	BLA1011-200_N_3
BLA1011-200_N_3	20010302	Product specification	-	9397 750 08109	BLA1011-200_N_2
BLA1011-200_N_2	20001201	Product specification	-	9397 750 07638	BLA1011-200_N_1
BLA1011-200_N_1	20000906	Product specification	-	9397 750 07326	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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