AR201110

CLL3H0914L(S)-700, 960 - 1215 MHz

AMPLEON

Application Report

v1.0 — 28th May 2020

Document information			
Status Company Public			
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Abstract	Measurement results of a Class-AB design for the 960 - 1215 MHz band with the CLL3H0914L(S)-700		

CLL3H0914L(S)-700

960 - 1215 MHz

1. Revision History

Table 1: Report revisions

Revision	Date	Description	Author
1.0	20200519	Initial document	Hans Mollee

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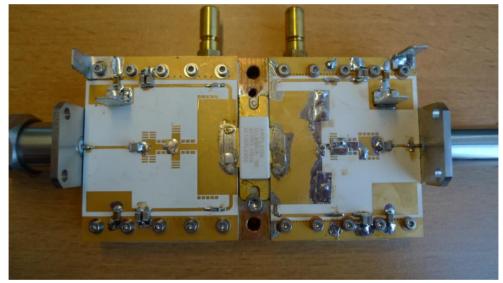
960 - 1215 MHz

5. General description

This report presents the measurement results of the Class-AB demo AR201110. The device used is a CLL9H0914L(S)-700, 3RD generation GaN in a ceramic SOT502-package. The demo is designed for the frequency band 960 - 1215 MHz

The PCB has been designed on Rodgers RO4360G2, h=0.61mm, ϵ_R =6.15, 35um double sided copper.

Supply voltage (drain-source) is 52V to overcome voltage drop in the power supply in the measurement set-up. The actual drain supply however is 50V. The gate bias voltage is connected to the Vg terminals on the input board. To set the drain quiescent current, first apply -5V at the gate terminal before connecting the 50V drain voltage. Then slowly increase V_{GS} to approx. -2.9V at which point the I_{DQ} will be 100 mA.





CLL3H0914L(S)-700 960 - 1215 MHz

6.1 Performance Details

The pulse format used is a $50~\mu s$ pulse with a duty cycle of 10%. The power sweep was performed up to 1.5 dB gain compression.

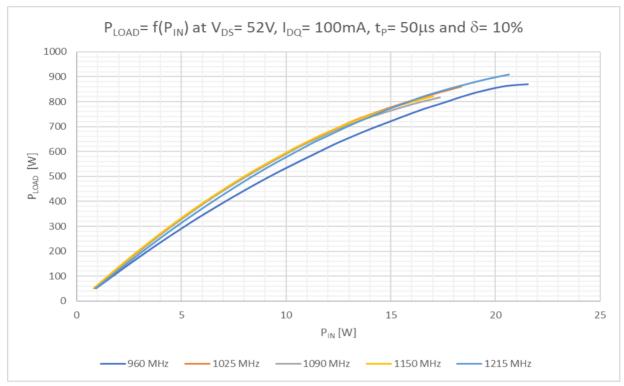


Figure 1 PLOAD VS PIN

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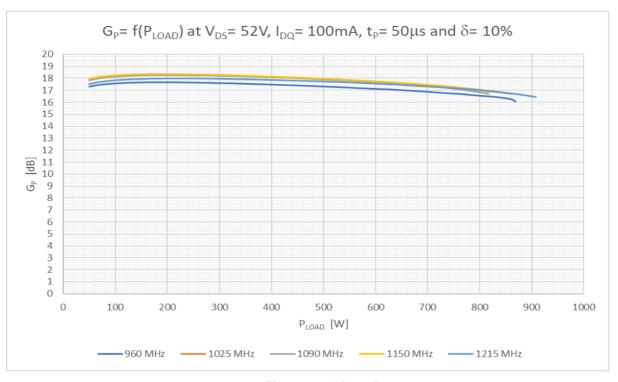


Figure 2 Gain vs PLOAD

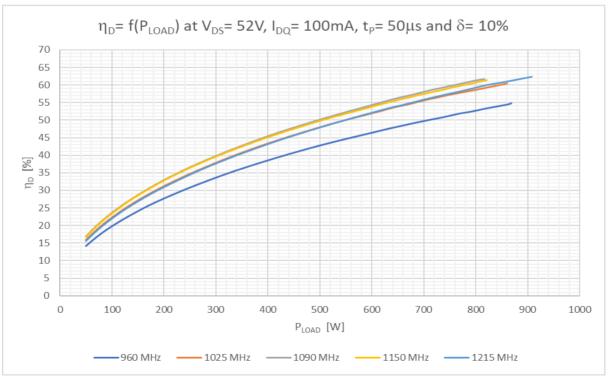


Figure 3 Drain efficiency vs PLOAD

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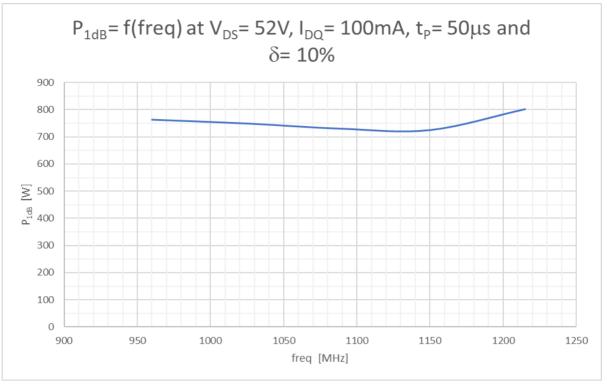


Figure 4 1dB Gain compression vs frequency

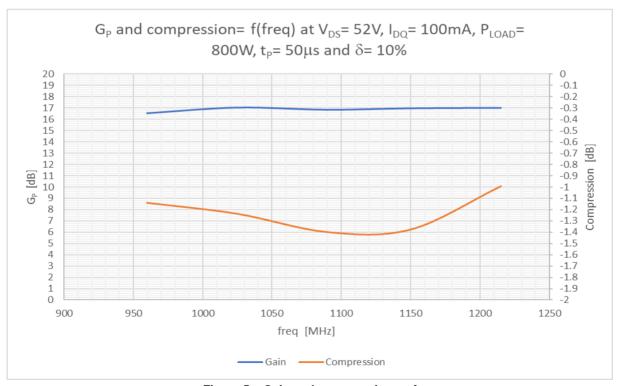


Figure 5 Gain and compression vs frequency

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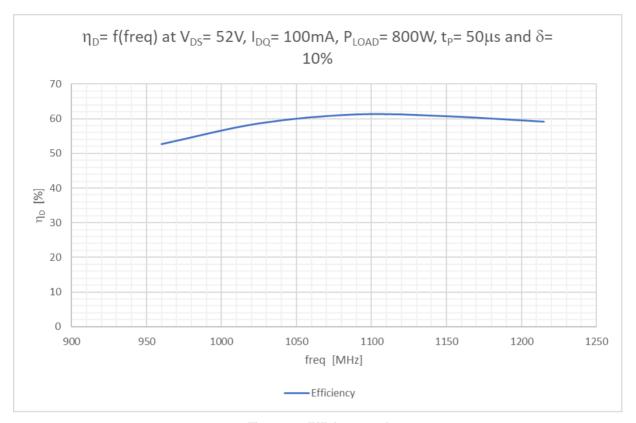


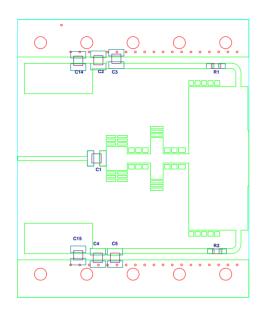
Figure 6 Efficiency vs frequency

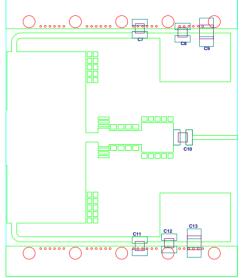
Parameter	960 MHz	1025 MHz	1090 MHz	1150 MHz	1215 MHz
P _{1dB}	764	750	730	725	802
Compression at 600 W	0.56	0.56	0.57	0.63	0.40
Compression at 800 W	1.13	1.23	1.39	1.37	1.00
G _P at P _{1dB}	16.7	17.2	17.2	17.3	17.0
η _D at P _{1dB}	51.9	57.8	59.1	58.5	59.9

Table 1: Performance summary

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7 Hardware





Components list application circuit.

C1, C3, C5, C7, C10, C11 430 pF ATC800B

C2, C4, C8, C12 1 nF ATC100B

R1, R2 10Ω 0603 SMD Resistor

PCB Material: Rogers 4360G2, thickness 0.61 mm (24 mil) or equivalent, ε_R = 6.15, Cu = 35 micron

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Table 2: Board specifications

Parameter	Value
Manufacturer	Rogers
Туре	RO4360G
Thickness	24 mil, 0.61 mm
Layers	2, top/bottom. Bottom all copper

Table 3: Device specifics

Parameter	Value		
Manufacturer	Ampleon		
Device	CLL3H0914L-700		
Marking	CLL3H0914L-700, wk2011-10836		
Comments	Engineering sample		

CLL3H0914L(S)-700 960 - 1215 MHz

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