BLS7G3135L-350P; BLS7G3135LS-350P LDMOS S-band radar power transistor

AMPLEON

Rev. 4 — 1 September 2015

Product data sheet

Product profile 1.

1.1 General description

350 W LDMOS power transistor intended for radar applications in the 3.1 GHz to 3.5 GHz range.

Table 1. Typical performance

Typical RF performance at $T_{case} = 25$ °C; $t_p = 300 \ \mu s$; $\delta = 10$ %; $I_{Da} = 200 \ mA$; in a class-AB production test circuit.

Test signal	f	V _{DS}	PL	G _p	η_{D}	t _r	t _f
	(GHz)	(V)	(W)	(dB)	(%)	(ns)	(ns)
pulsed RF	3.1	32	350	12	43	5	5
	3.3	32	350	12	43	5	5
	3.5	32	350	10	39	5	5

1.2 Features and benefits

- Easy power control
- Integrated ESD protection
- High flexibility with respect to pulse formats
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (3.1 GHz to 3.5 GHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

S-Band power amplifiers for radar applications in the 3.1 GHz to 3.5 GHz frequency range

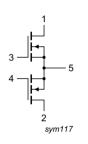
Pinning information 2.

Table 2 Pinning

Table 2.	Filling			
Pin	Description		Simplified outline	Graphic symbol
BLS7G3	135L-350P (SOT539A)			
1	drain1			,
2	drain2		1 2	<u>1</u> ك.
3	gate1		5	3
4	gate2		3 4	5
5	source	[1]		4
				2 sym117

BLS7G31	BLS7G3135LS-350P (SOT539B)				
1	drain1				
2	drain2				
3	gate1				
4	gate2				
5	source	<u>[1]</u>			





Ordering information

Table 3. **Ordering information**

Type number	Package				
	Name	Description	Version		
BLS73135L-350P	-	flanged balanced ceramic package; 2 mounting holes; 4 leads	SOT539A		
BLS73135LS-350P	-	earless flanged balanced ceramic package; 4 leads	SOT539B		

Limiting values

Table 4. **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Min	Max	Unit
V_{DS}	drain-source voltage	-	65	V
V_{GS}	gate-source voltage	-0.5	+11	V
T _{stg}	storage temperature	-65	+150	°C
Tj	junction temperature	<u>[1]</u> _	225	°C

Continuous use at maximum temperature will affect the reliability. For details refer to the on-line MTF calculator.

^[1] Connected to flange.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
Z _{th(j-mb)} transient thermal impeda mounting base	transient thermal impedance from junction to	T_{case} = 85 °C; P_L = 350 W		
	mounting base	t_p = 300 μ s; δ = 10 %	0.1	K/W
		$t_p = 100 \ \mu s; \ \delta = 20 \ \%$	0.09	K/W
		t_p = 100 μ s; δ = 10 %	0.07	K/W
		t_p = 200 μ s; δ = 10 %	0.09	K/W

6. Characteristics

Table 6. DC characteristics

 $T_i = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 2.2 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V_{DS} = 10 V; I_{D} = 220 mA	1.5	1.9	2.3	V
I _{DSS}	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	2.8	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 V;$ $V_{DS} = 10 V$	-	39	-	Α
I _{GSS}	gate leakage current	$V_{GS} = 11 \text{ V}; V_{DS} = 0 \text{ V}$	_	-	280	nA
9fs	forward transconductance	$V_{DS} = 10 \text{ V}; I_{D} = 11 \text{ A}$	_	16.2	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 7.7 A$	-	0.065	-	Ω

Table 7. RF characteristics

Test signal: pulsed RF; t_p = 300 μ s; δ = 10 %; RF performance at V_{DS} = 32 V; I_{Dq} = 200 mA; T_{case} = 25 °C; unless otherwise specified, in a class-AB production circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
At frequency	y of 3.1 GHz					
Gp	power gain	$P_{L} = 350 \text{ W}$	10.5	12	-	dB
RLin	input return loss	$P_{L} = 350 \text{ W}$	-	-6	-	dB
η_{D}	drain efficiency	P _L = 350 W	38	43	-	%
P _{droop(pulse)}	pulse droop power	P _L = 350 W	-	0.2	0.3	dB
t _r	rise time	P _L = 350 W	-	5	50	ns
t _f	fall time	P _L = 350 W	-	5	50	ns
At frequency	y of 3.3 GHz					
G _p	power gain	P _L = 350 W	10.5	12	-	dB
RLin	input return loss	P _L = 350 W	-	-6	-	dB
η_{D}	drain efficiency	P _L = 350 W	38	43	-	%
P _{droop(pulse)}	pulse droop power	P _L = 350 W	-	0.2	0.3	dB
t _r	rise time	P _L = 350 W	-	5	50	ns
t _f	fall time	P _L = 350 W	-	5	50	ns
At frequency	y of 3.5 GHz					
G _p	power gain	P _L = 320 W	8.5	10	-	dB
RLin	input return loss	P _L = 320 W	-	-9	-	dB
η_{D}	drain efficiency	P _L = 320 W	35	39	-	%
P _{droop(pulse)}	pulse droop power	P _L = 320 W	-	0.2	0.3	dB
t _r	rise time	P _L = 320 W	-	5	50	ns
t _f	fall time	P _L = 320 W	-	5	50	ns

7. Application information

7.1 Ruggedness in class-AB operation

The BLS7G3135L-350P and the BLS7G3135LS-350P are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 32 V; I_{Dq} = 200 mA; P_L = 350 W; t_p = 300 μ s; δ = 10 %

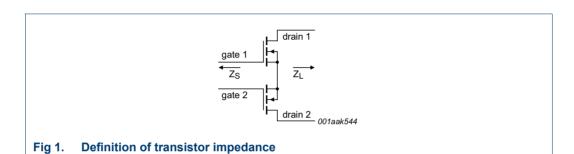
7.2 Impedance information

Table 8. Typical impedance

Measured load-pull data. Typical values unless otherwise specified.

f	Z _S [1]	Z _L [1]
(GHz)	(Ω)	(Ω)
3.1	1.8 – 7.2j	3.6 – 6.3j
3.2	1.6 – 7.1j	4.4 – 6.7j
3.3	2.2 – 8.2j	4.8 – 5.8j
3.4	3.1 – 9.7j	5.7 – 6.2j
3.5	3.6 – 11.6j	6.5 – 4.6j

[1] Impedances are taken at a single half of the push-pull transistor.



7.3 Test circuit information

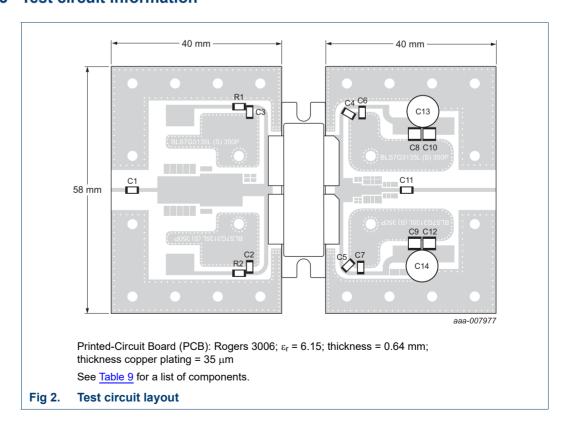
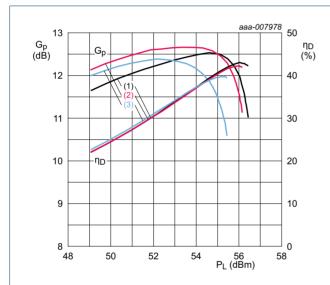


Table 9. List of components

For test circuit see Figure 2.

Component	Description	Value	Remarks
C1, C2, C3	multilayer ceramic chip capacitor	8.2 pF	ATC100A
C4, C5, C11	multilayer ceramic chip capacitor	15 pF	ATC800B
C6, C7	multilayer ceramic chip capacitor	100 pF	ATC800A
C8, C9	multilayer ceramic chip capacitor	1 μF, 50 V	TDK
C10, C12	multilayer ceramic chip capacitor	10 μF, 50 V	TDK
C13, C14	electrolytic capacitor	220 μF, 63 V	
R1, R2	SMD resistor	10 Ω	

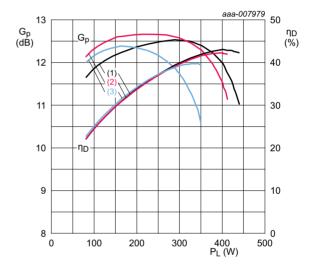
7.4 Graphical data



 V_{DS} = 32 V; I_{Dq} = 200 mA.

- (1) f = 3100 MHz
- (2) f = 3300 MHz
- (3) f = 3500 MHz

Fig 3. Power gain and drain efficiency as function of output power; typical values



 $V_{DS} = 32 \text{ V}; I_{Dq} = 200 \text{ mA}.$

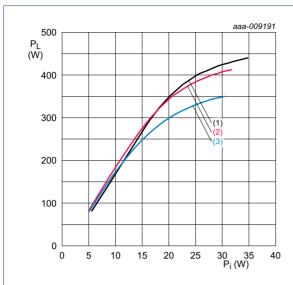
- (1) f = 3100 MHz
- (2) f = 3300 MHz
- (3) f = 3500 MHz

Fig 4. Power gain and drain efficiency as function of output power; typical values

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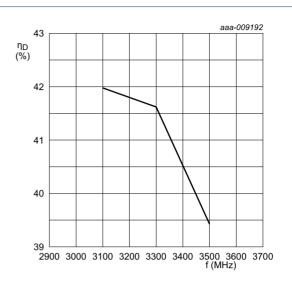
LDMOS S-band radar power transistor



 V_{DS} = 32 V; I_{Dq} = 200 mA; t_p =300 $\mu s;$ δ = 10 %.

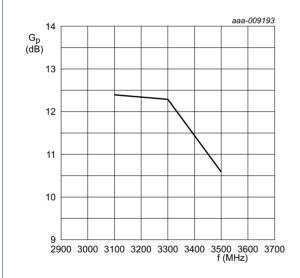
- (1) f = 3100 MHz
- (2) f = 3300 MHz
- (3) f = 3500 MHz

Fig 5. Output power as a function of input power; typical values



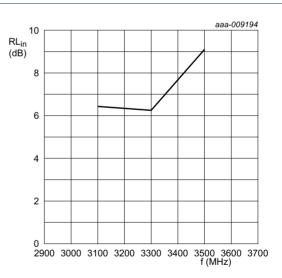
 V_{DS} = 32 V; I_{Dq} = 200 mA; t_p = 300 $\mu s;$ δ = 10 %; P_L = 350 W.

Fig 6. Drain efficiency as a function of frequency; typical values



 V_{DS} = 32 V; I_{Dq} = 200 mA; t_p = 300 $\mu s; \, \delta$ = 10 %; P_L = 350 W.

Fig 7. Power gain as a function of frequency; typical values



 V_{DS} = 32 V; I_{Dq} = 200 mA; t_p = 300 μ s; δ = 10 %; P_{t_p} = 350 W

Fig 8. Input return loss as a function of frequency; typical values

8. Package outline

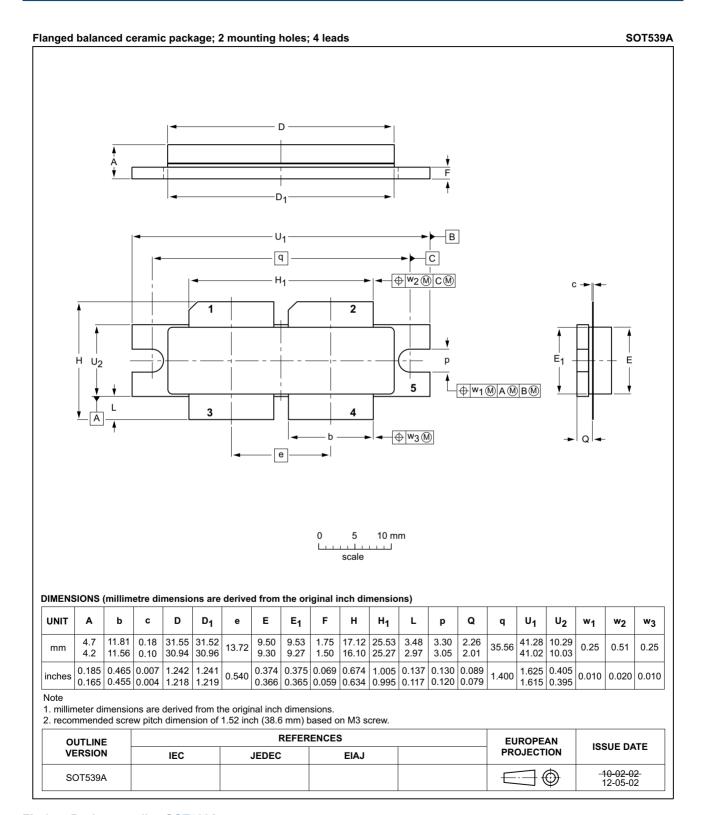


Fig 9. Package outline SOT539A

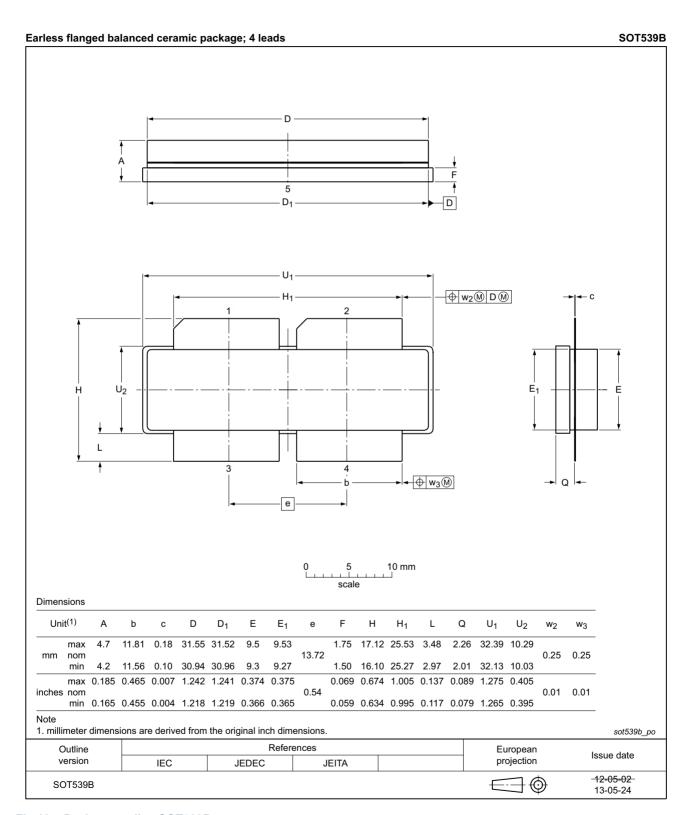


Fig 10. Package outline SOT539B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbreviations

Acronym	Description
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
S-Band	Short wave band
SMD	Surface Mounted Device
VSWR	Voltage Standing-Wave Ratio

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLS73135L-350P_7G3135LS-350P#4	20150901	Product data sheet		BLS73135L-350P_7G31 35LS-350P v.3
Modifications:	 The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. 			
	 Legal texts 	have been adapted to	the new company	y name where appropriate.
BLS73135L-350P_7G3135LS-350P v.3	20131029	Product data sheet	-	BLS73135L-350P_ 7G3135LS-350P v.2
BLS73135L-350P_7G3135LS-350P v.2	20130801	Objective data sheet	-	BLS73135L-350P_ 7G3135LS-350P v.1
BLS73135L-350P_7G3135LS-350P v.1	20121012	Objective data sheet	-	-

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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