# AR161048

BLS9G2934LS-400, 2900 to 3400MHz

**AMPLEON** 

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Application Report

Document information		
Status Company Public		
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Abstract Measurement results of a Class-AB design for the 2900 to 3400MHz band with the BLS9G2934LS-400		

BLS9G2934LS-400 2900 to 3400MHz

# 1. Revision History

Table 1: Report revisions

Revision	Date	Description	Author
1.0	20160331	Initial document	Hans Mollee

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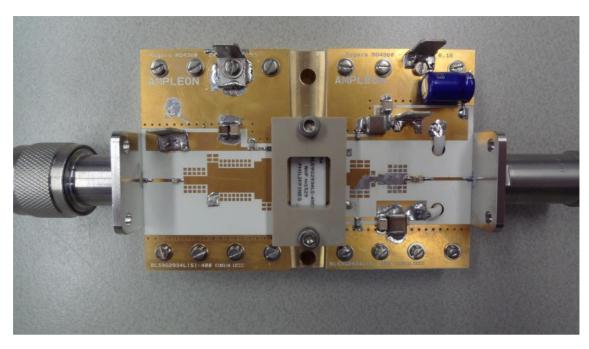
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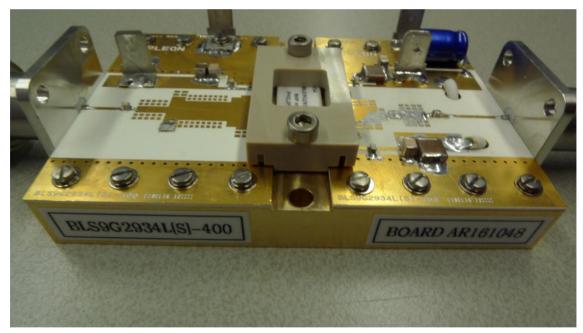
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# 5. General description

This report presents the measurement results of the Class-AB demo AR161048. The device used is a 9<sup>th</sup> generation LDMOS single ended package, the BLS9G2934LS-400. The presented demo is tuned for the frequency band 2900 to 3400MHz.

BLS9G2934LS-400 2900 to 3400MHz





The PCB has been designed on Rodgers RO4360G2, h=0.61mm,  $\epsilon_R$ =6.2, 35um double sided copper. Supply voltage (drain-source) is 32V. Gate bias voltage is connected to the Vg terminals on the input board. To set the drain quiescent current, slowly increase V<sub>GS</sub> until the I<sub>DQ</sub> will be 400 mA, starting at about 1V.

When switching of the RF-pulse a spike may appear on the drain supply due to the inductance and the fall time of the pulse. When using signal with a rapid fall time this spike may become (too) large. By placing two  $10\mu F$  SMD capacitors (C13 and C14) on the drain supply. These spikes will be reduced to virtually zero.

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## **Performance Details**

The pulse format used is a 300  $\mu$ s pulse with a duty cycle of 10%. The power sweep was performed up to 3 dB gain compression.

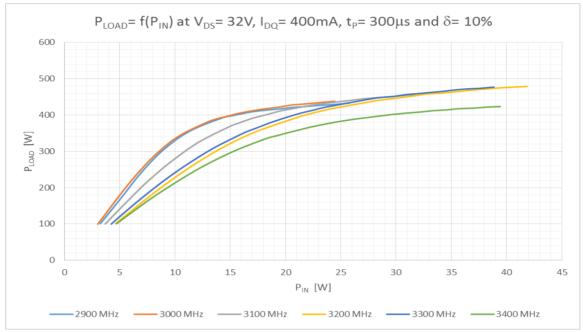


Figure 1 PLOAD VS PIN

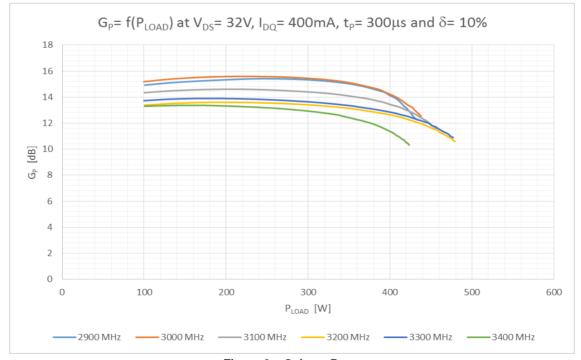


Figure 2 Gain vs PLOAD

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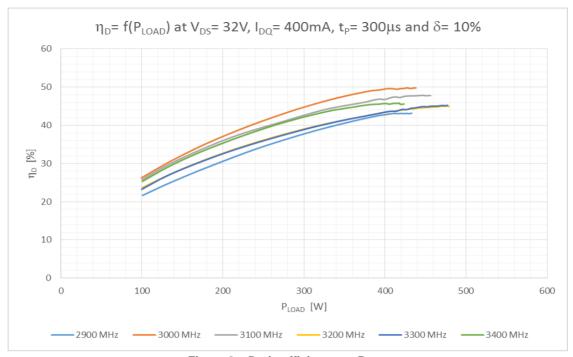


Figure 3 Drain efficiency vs PLOAD

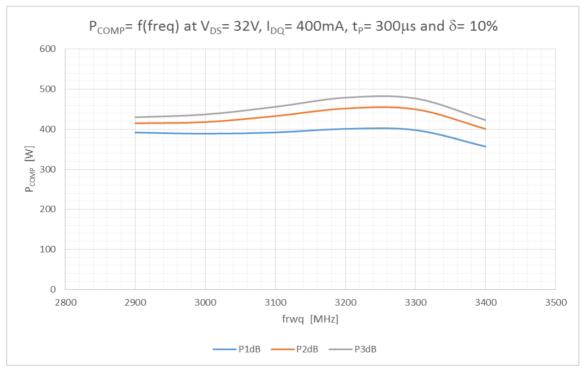


Figure 4 Compressed Power

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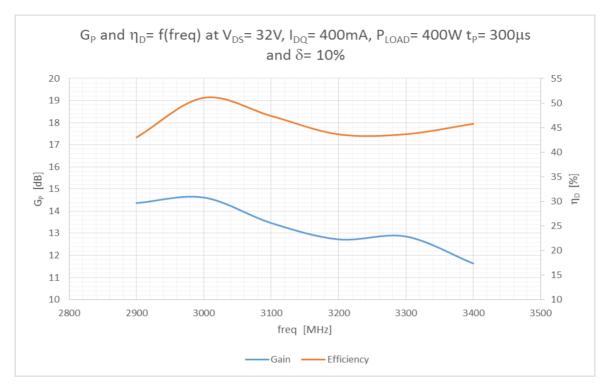
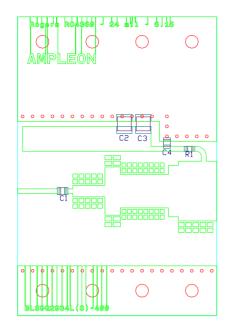
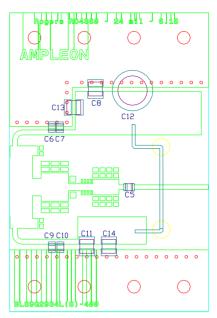


Figure 5 Performance at 400 W

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## 5.1 Hardware





Components list application circuit.		
C1, C4	10 pF	ATC100A
C6, C9	10 pF	ATC800A
C3, C8, C11	1 nF	ATC100B
C5	5.6 pF	ATC100A
C7, C10	-	-
C13, C14	10 μF	Murata GRM55DR61H106KA88L
C12	100 μF	63 V, Electrolytic capacitor
C2	4.7 μF	
R1	5 Ω	0603 SMD Resistor

PCB Material: Rogers 4360, thickness 0.61 mm (24 mil) or equivalent,  $\epsilon_R$  = 6.15, Cu = 35 micron

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### 5.2 Board material

Table 2: Board specifications

Parameter	Value
Manufacturer	Rogers
Туре	RO4360G2
Thickness	24 mil, 0.61 mm
Layers	2, top/bottom. Bottom all copper

# 5.3 Device markings

Table 3: Device specifics

Parameter	Value
Manufacturer	Ampleon
Device	BLS9G2934LS-400
Marking	BLS9G2934LS-400, m1529, Philippines
Comments	Engineering sample

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